

## Schematics Page Index (Title / Revision / Change Date)

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			Leon
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Size A4	Document Number MS02-1-01		Rev A
Date: Tuesday, February 22, 2005		Sheet 00 of 43	

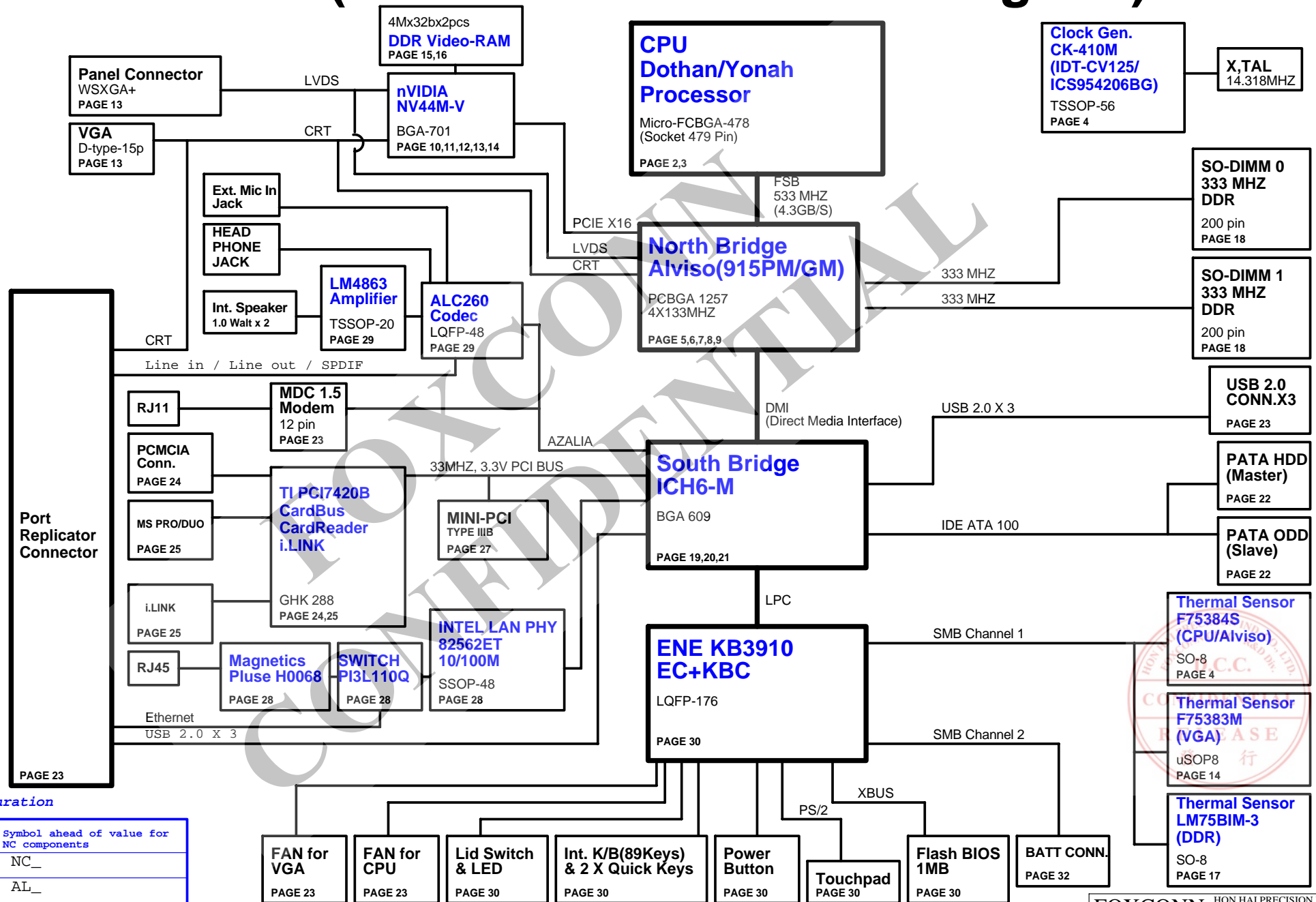
Project Code &amp; Schematics Subject: MS02 M/B-FUBAI

PCB P/N: 1P-0052100-8010

Project Code &amp; Schematics Subject: MS02 M/B-NAN YA

PCB P/N: 1P-0052200-8010

# MS02(915PM/GM+Gfx Block Diagram)



## BOM configuration

	Symbol ahead of value for NC components
BOTH	NC_
915GM + NV44M	AL_
915GM	NV_
Hynix	H_NV_
Samsung	S_NV_

# Dothan

1 OF 3

REQUEST  
PHASE  
SIGNALS

DATA  
PHASE  
SIGNALS

ERROR  
SIGNALS

ARBITRATION  
PHASE  
SIGNALS

SNOOP PHASE  
SIGNALS

RESPONSE  
PHASE  
SIGNALS

PC  
COMPATIBILITY  
SIGNALS

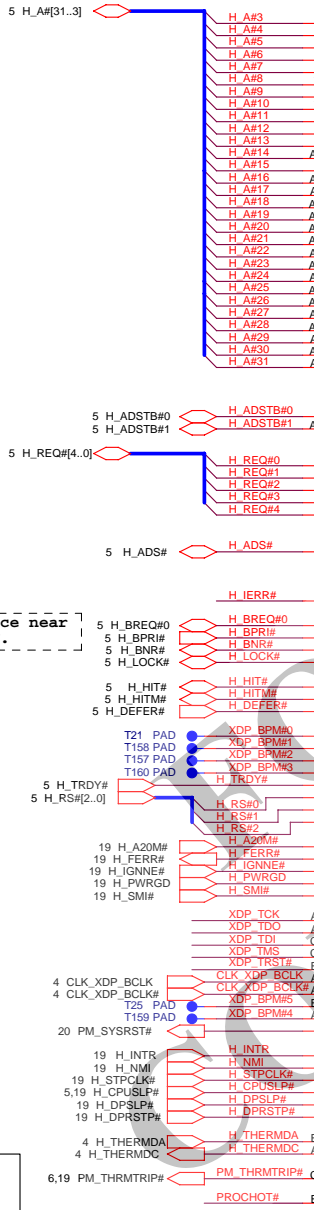
DIAGNOSTIC  
& TEST  
SIGNALS

EXECUTION  
CONTROL  
SIGNALS

THERMAL DIODE

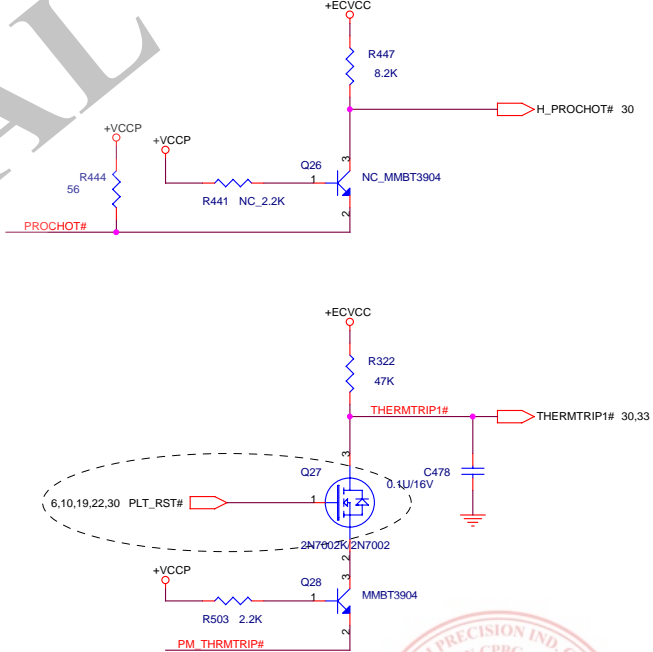
U30A

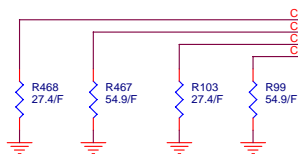
Dothan Processor



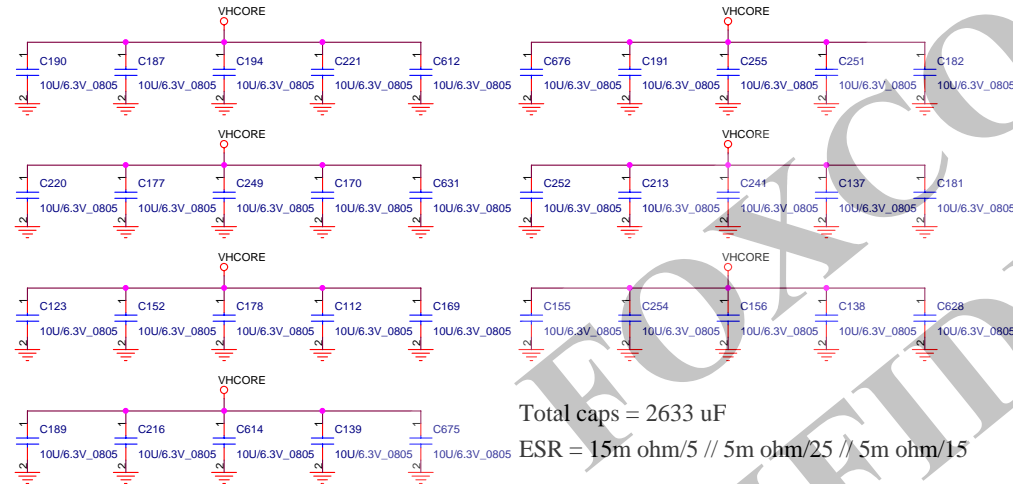
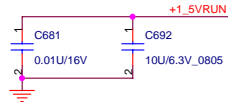
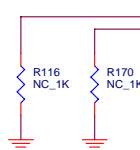
Place near  
CPU.

PM\_THRMTRIP#  
should connect to  
ICH6-M and ALVISO  
without T-ing (No  
stub)

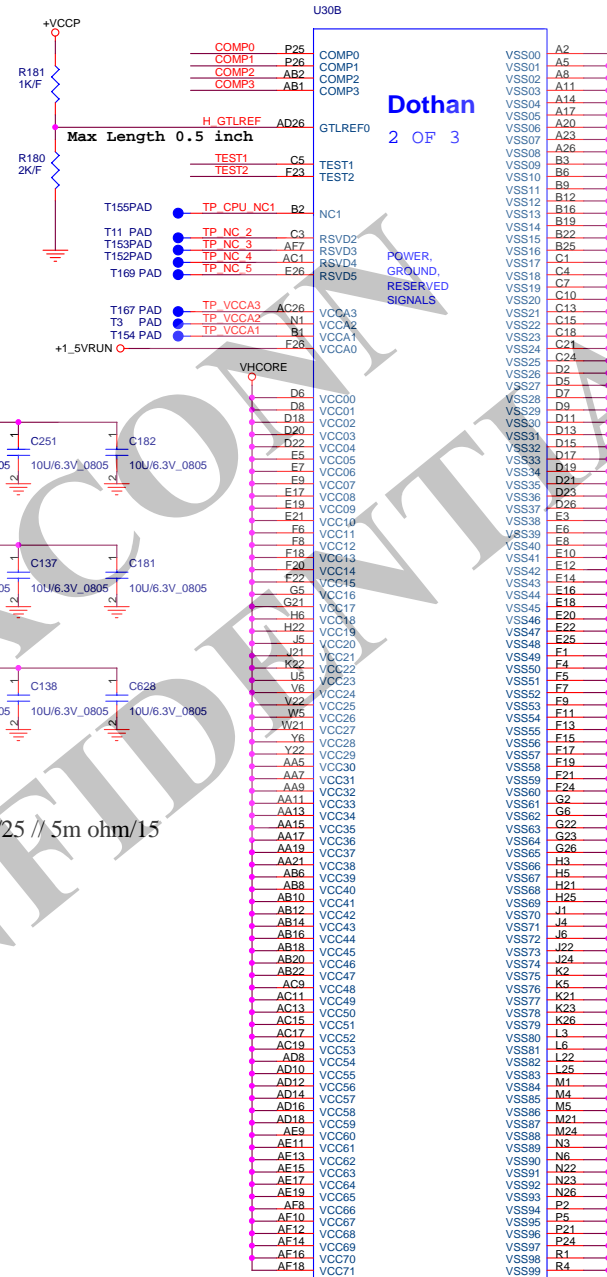
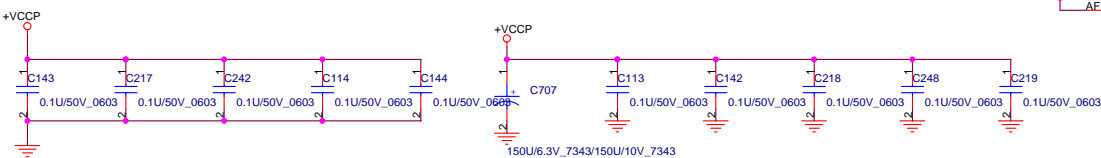




Place pull-down resistors within 0.5" of COMP pins

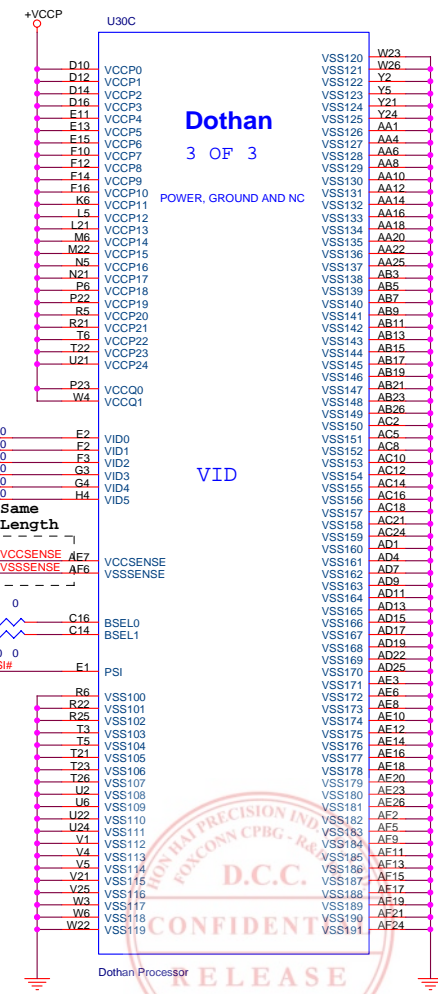


Total caps = 2633 uF  
ESR = 15m ohm/5 // 5m ohm/25 // 5m ohm/15



Dothan  
2 OF 3

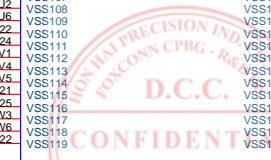
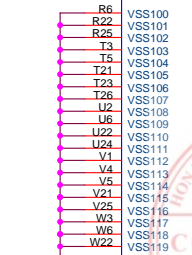
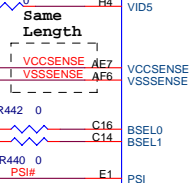
POWER,  
GROUND,  
RESERVED  
SIGNALS



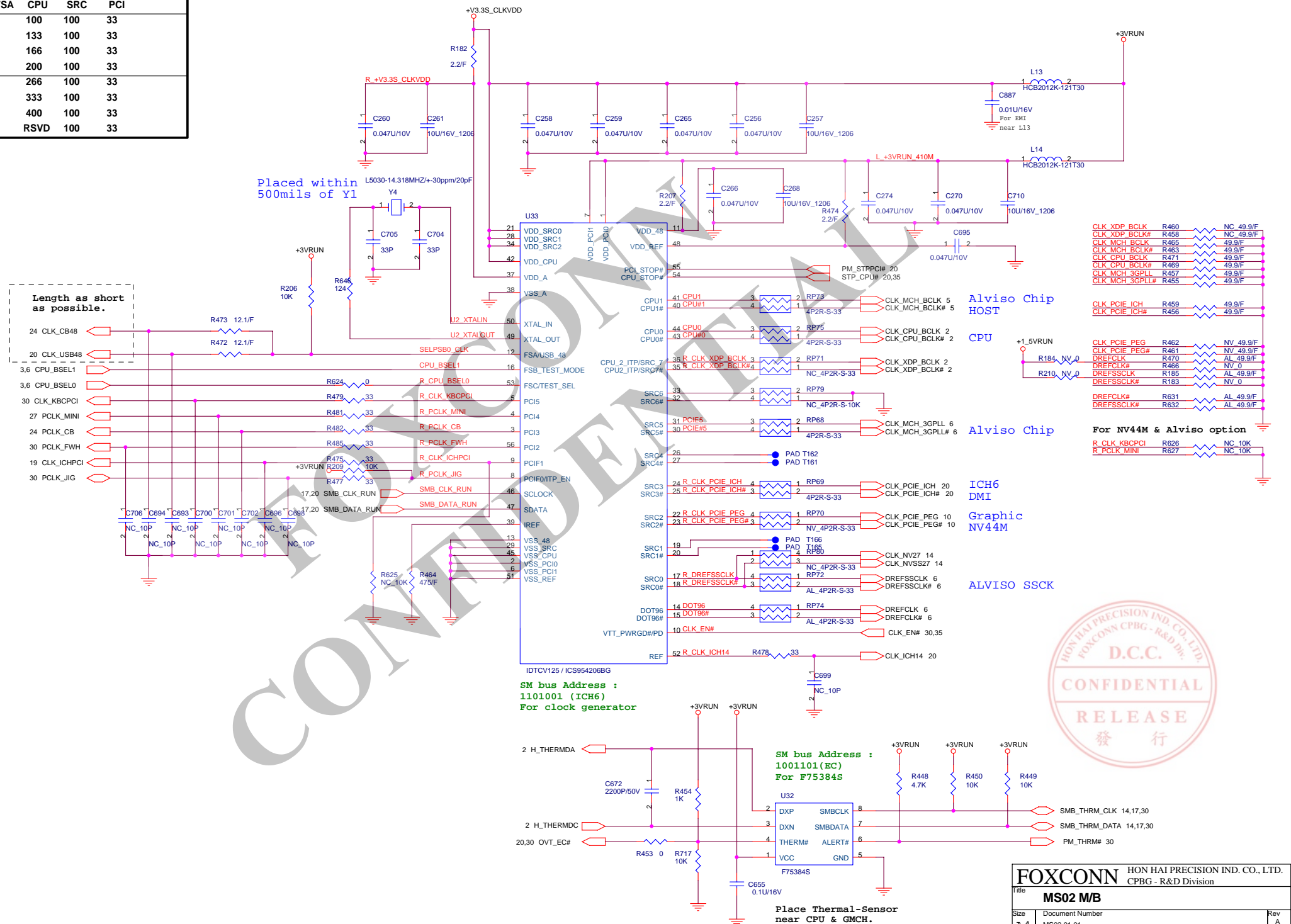
Dothan  
3 OF 3

POWER, GROUND AND NC

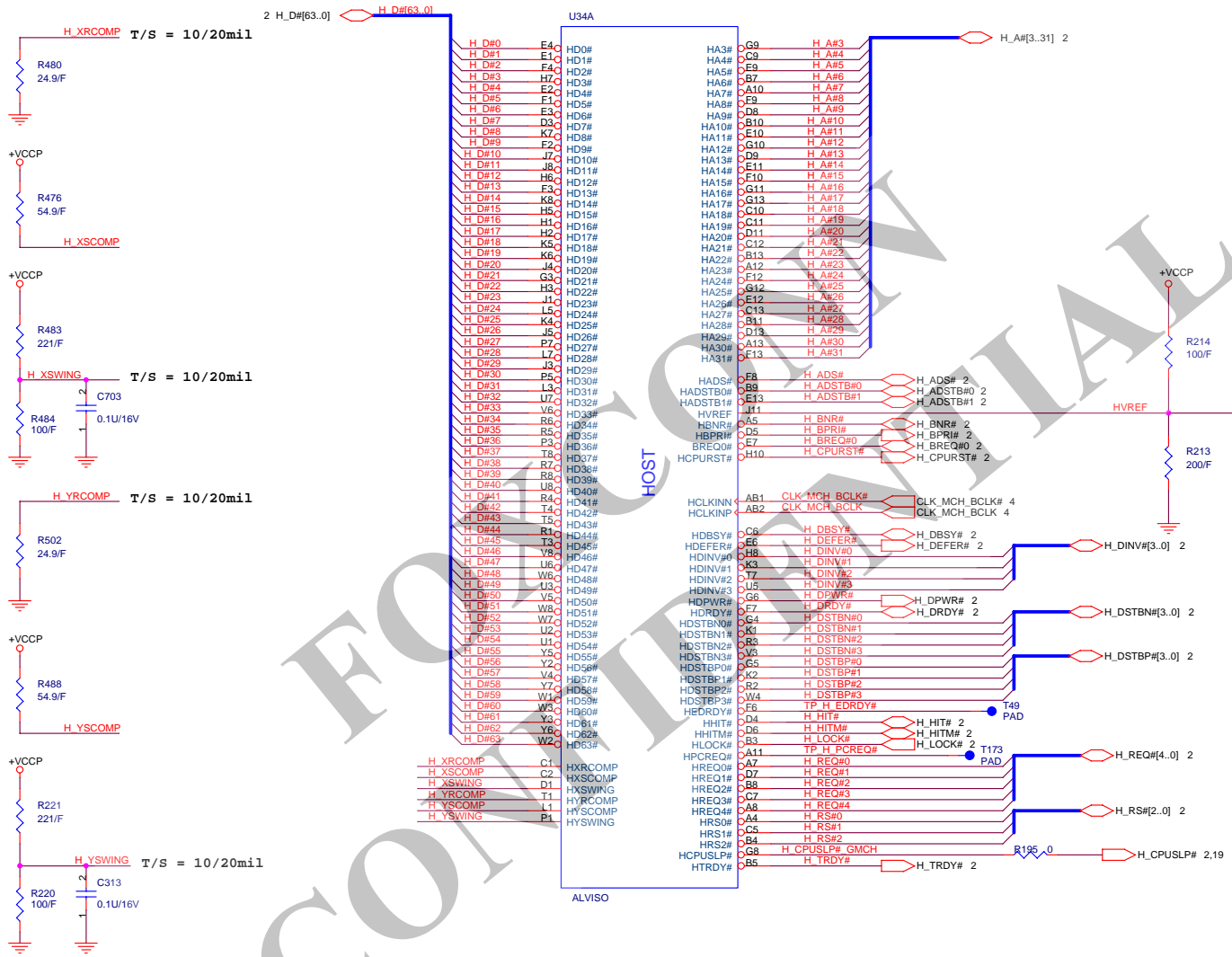
VID



FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

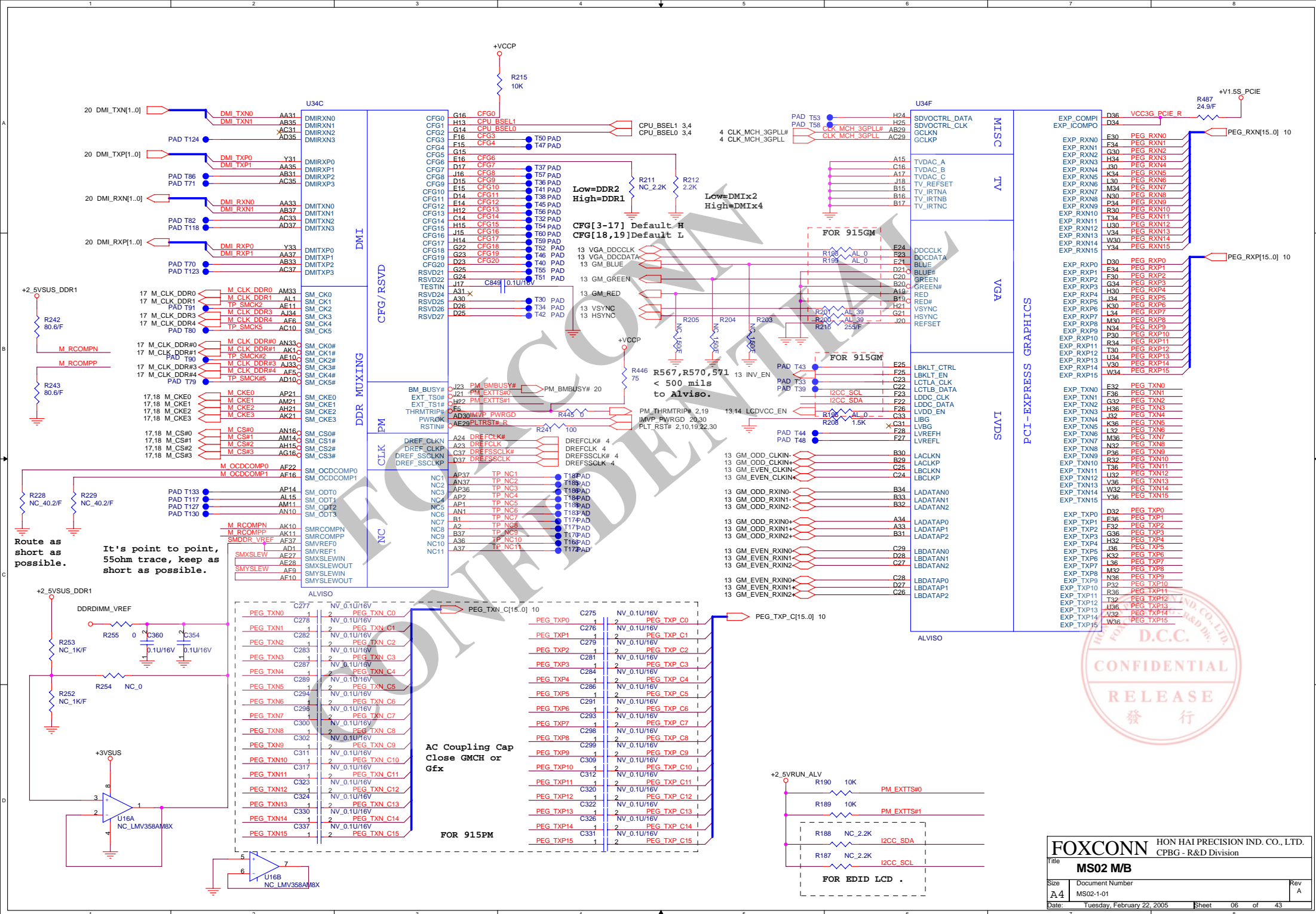


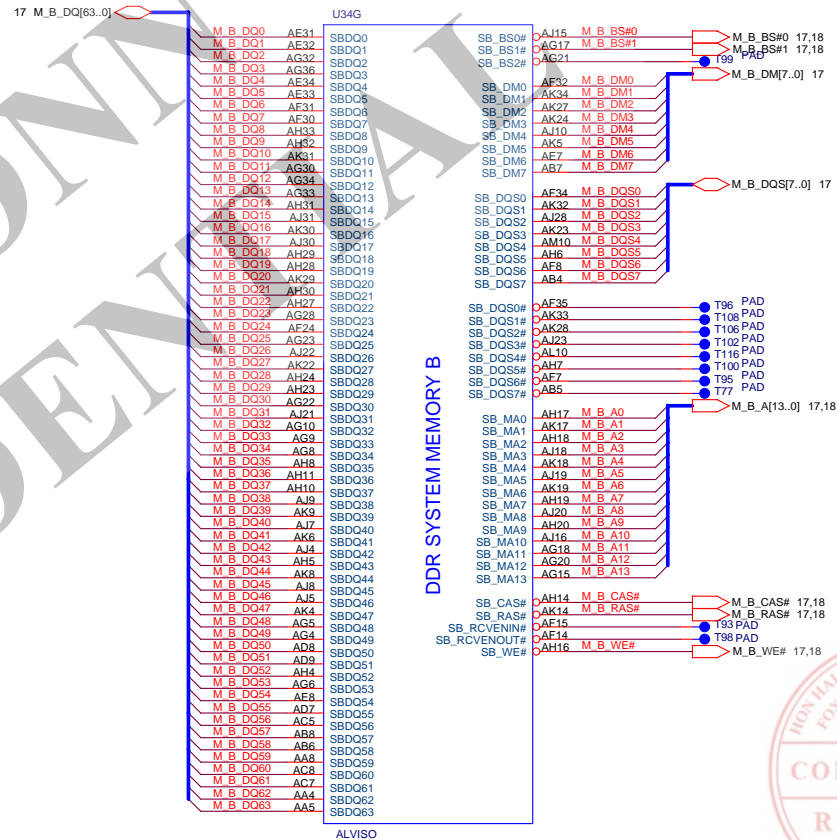
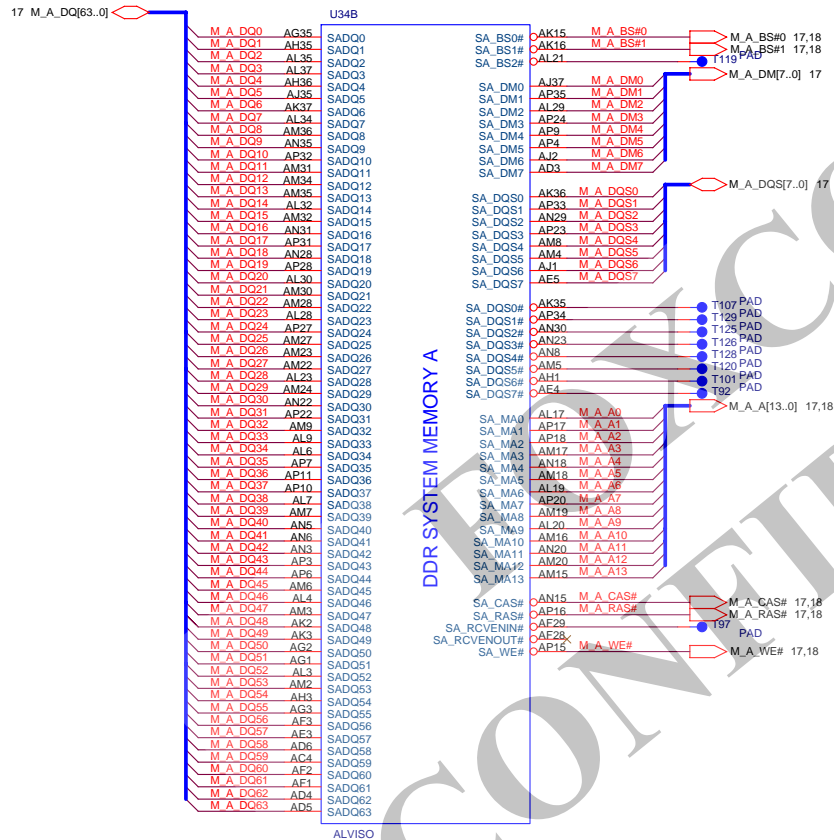




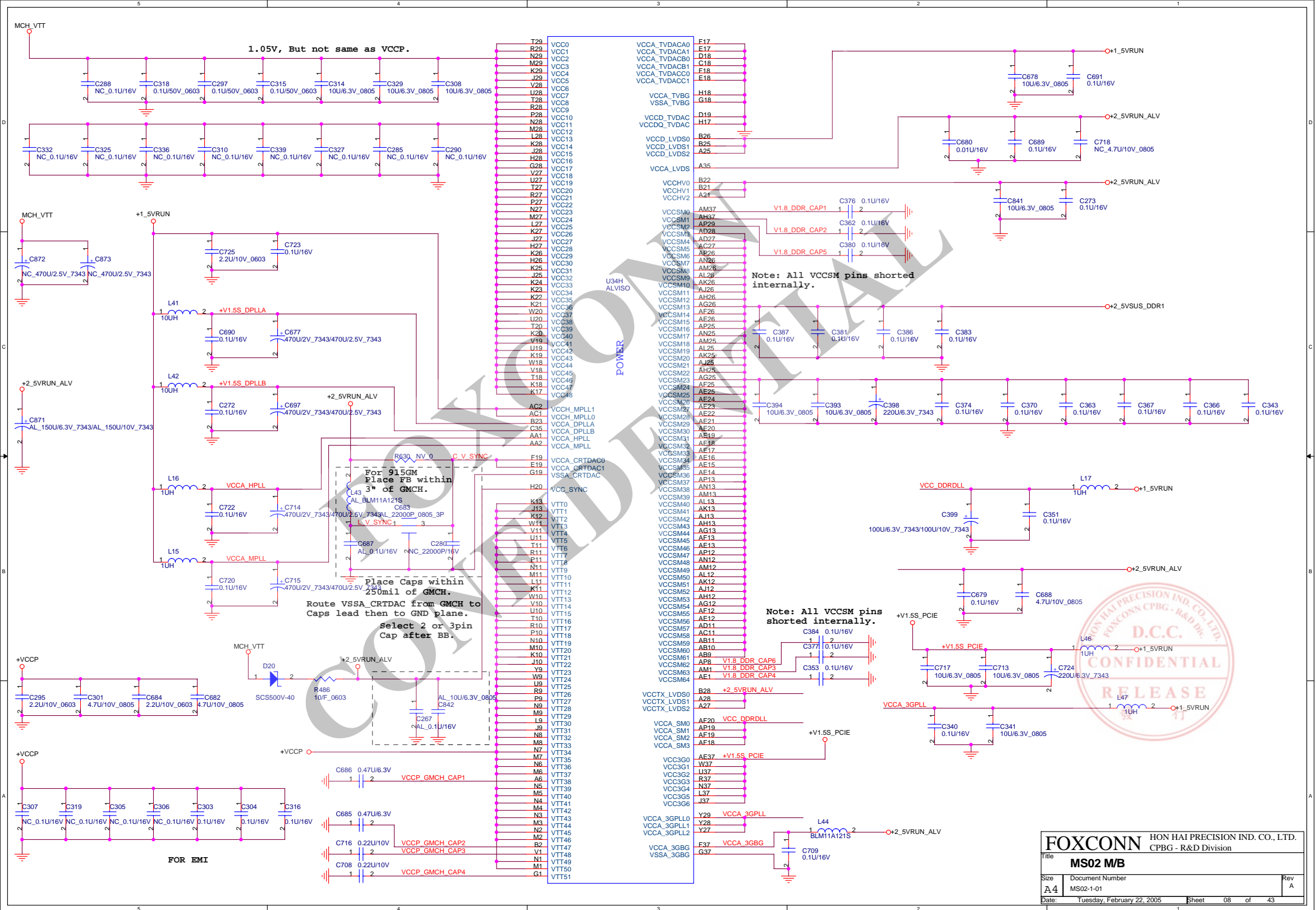
Place Cap. near GMCH  
within 100 mils.

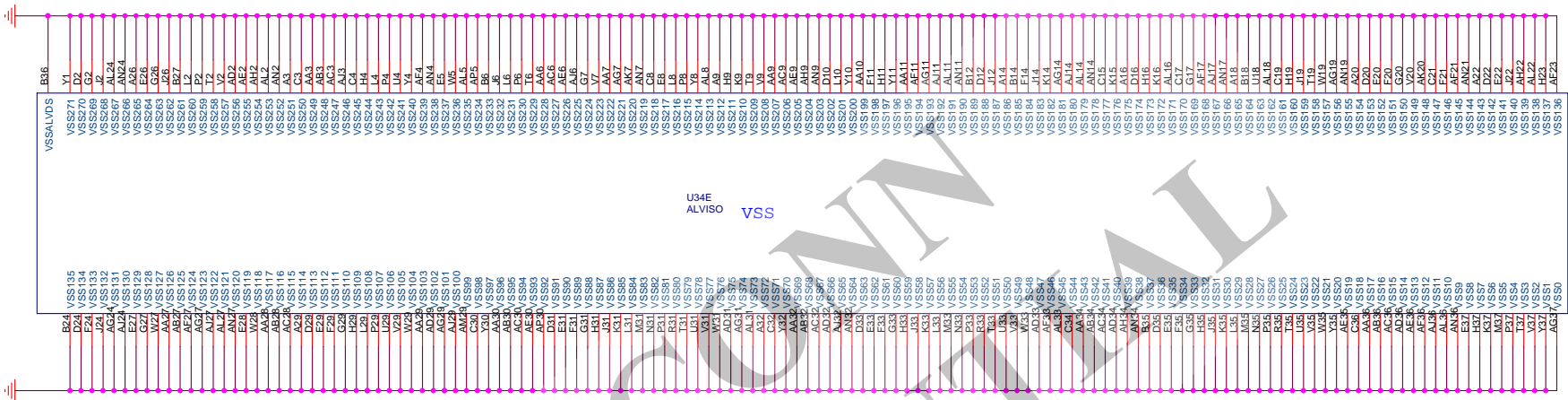
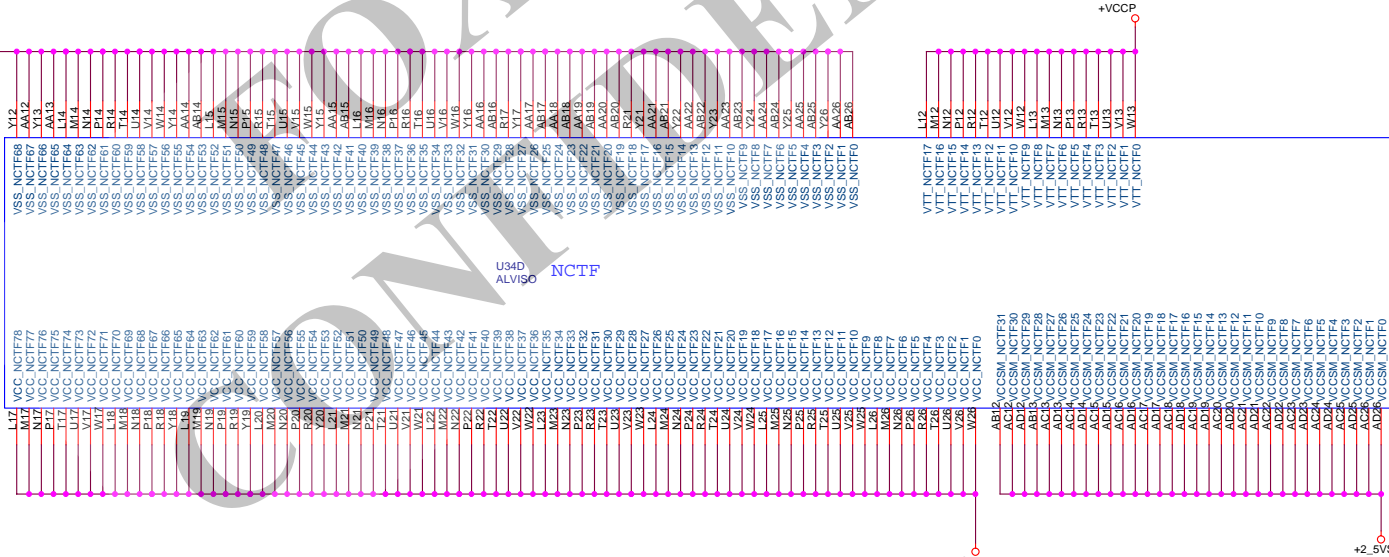


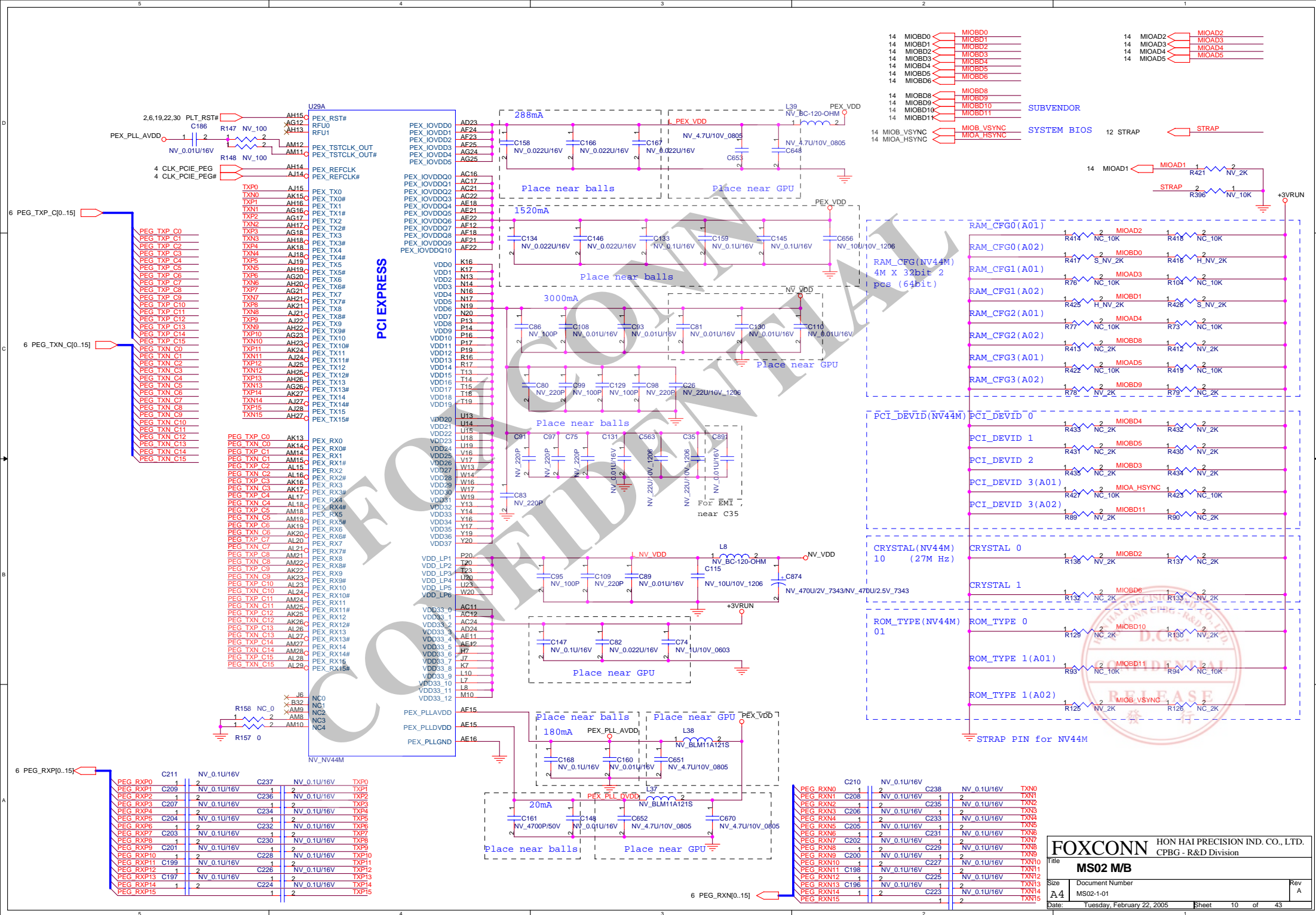


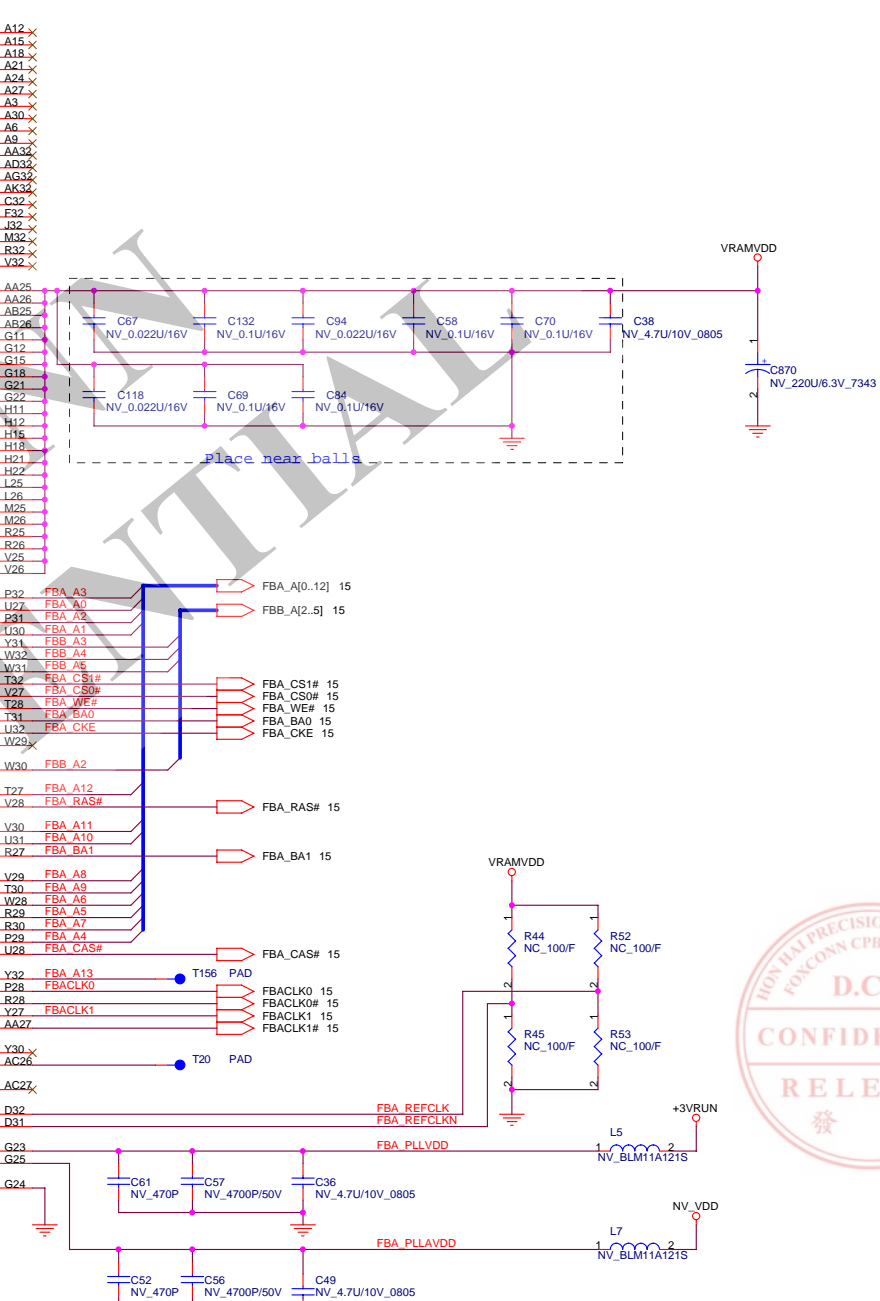
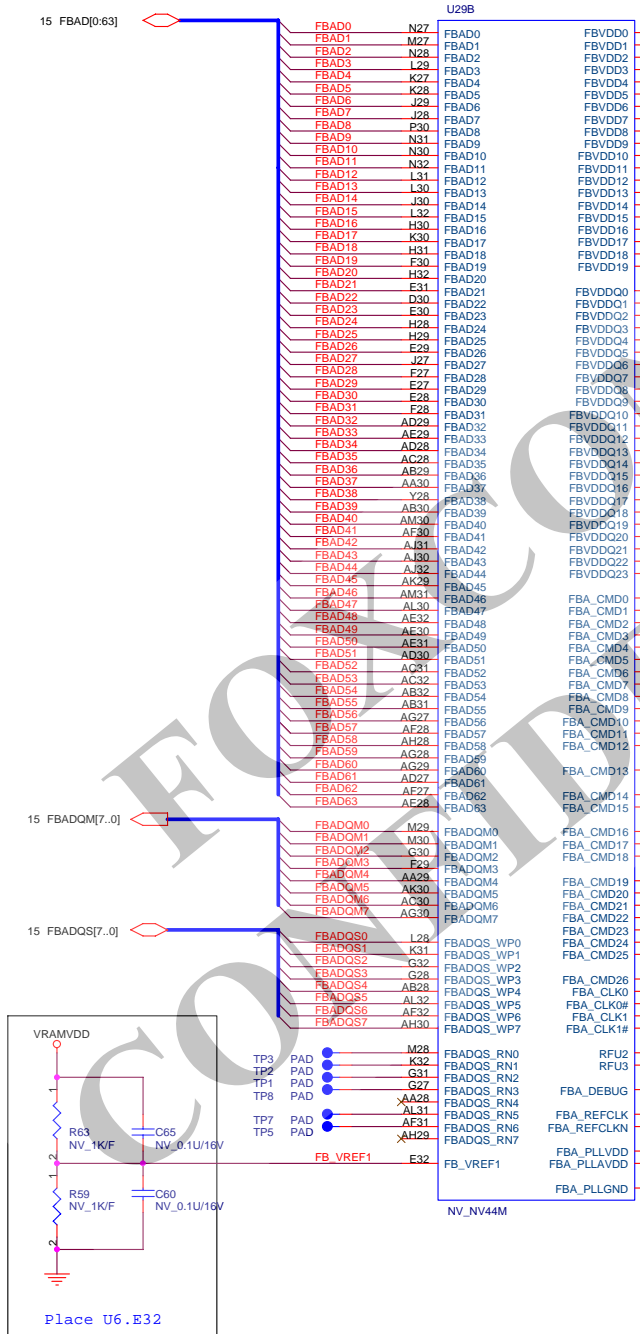
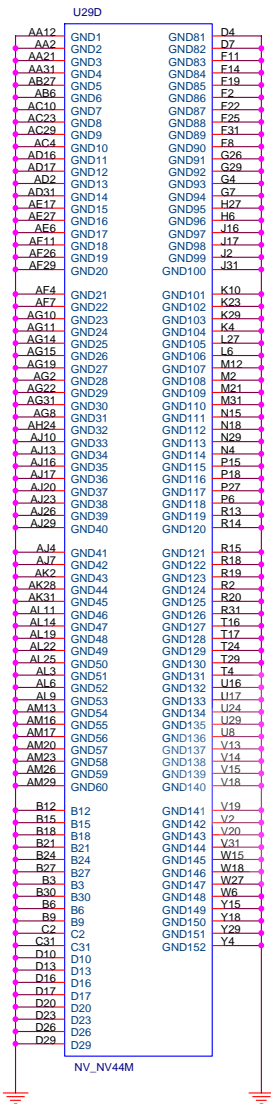


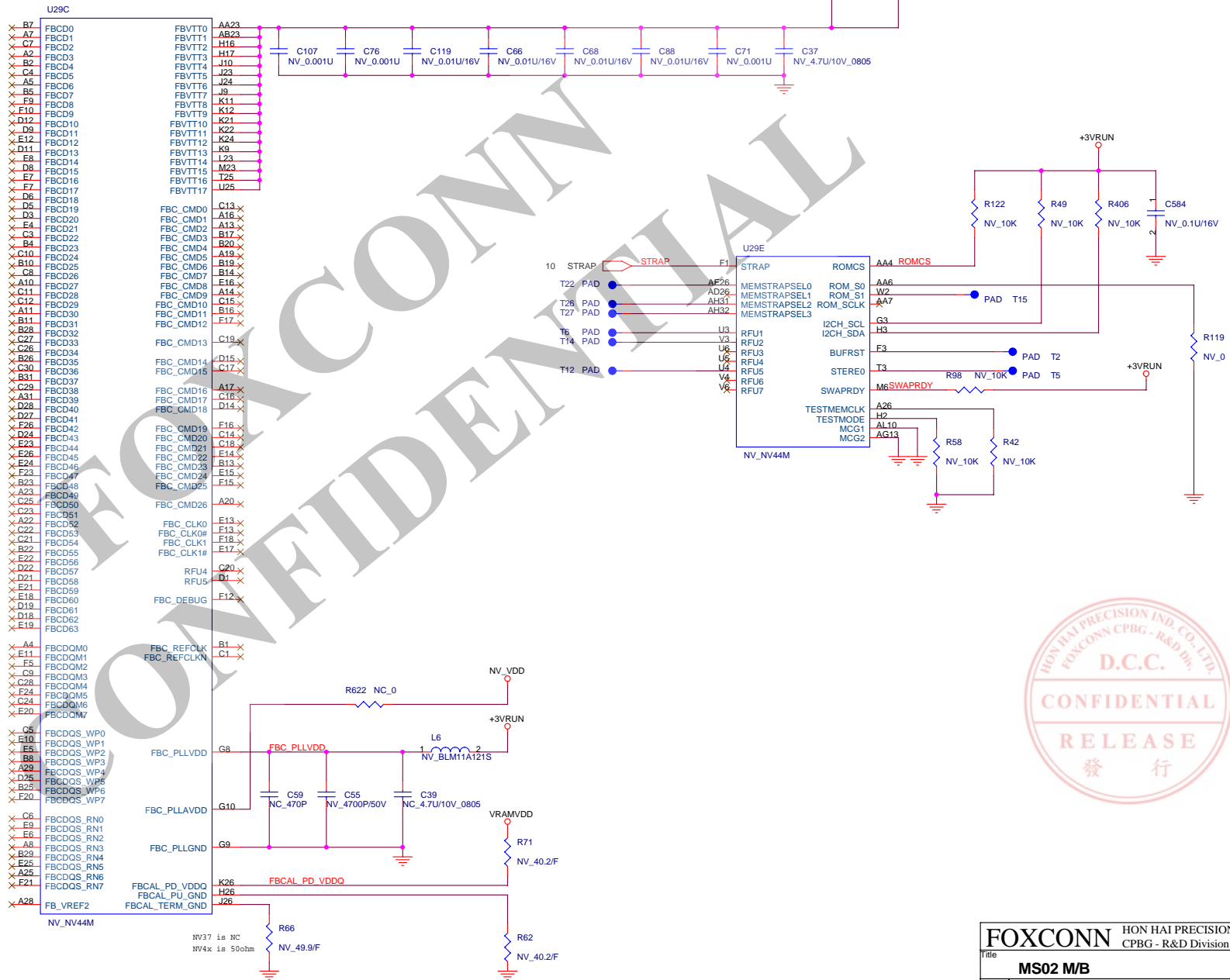






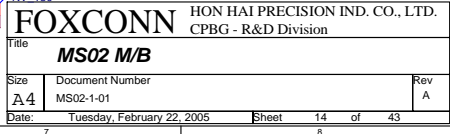


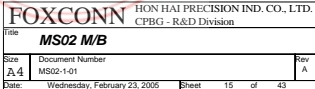




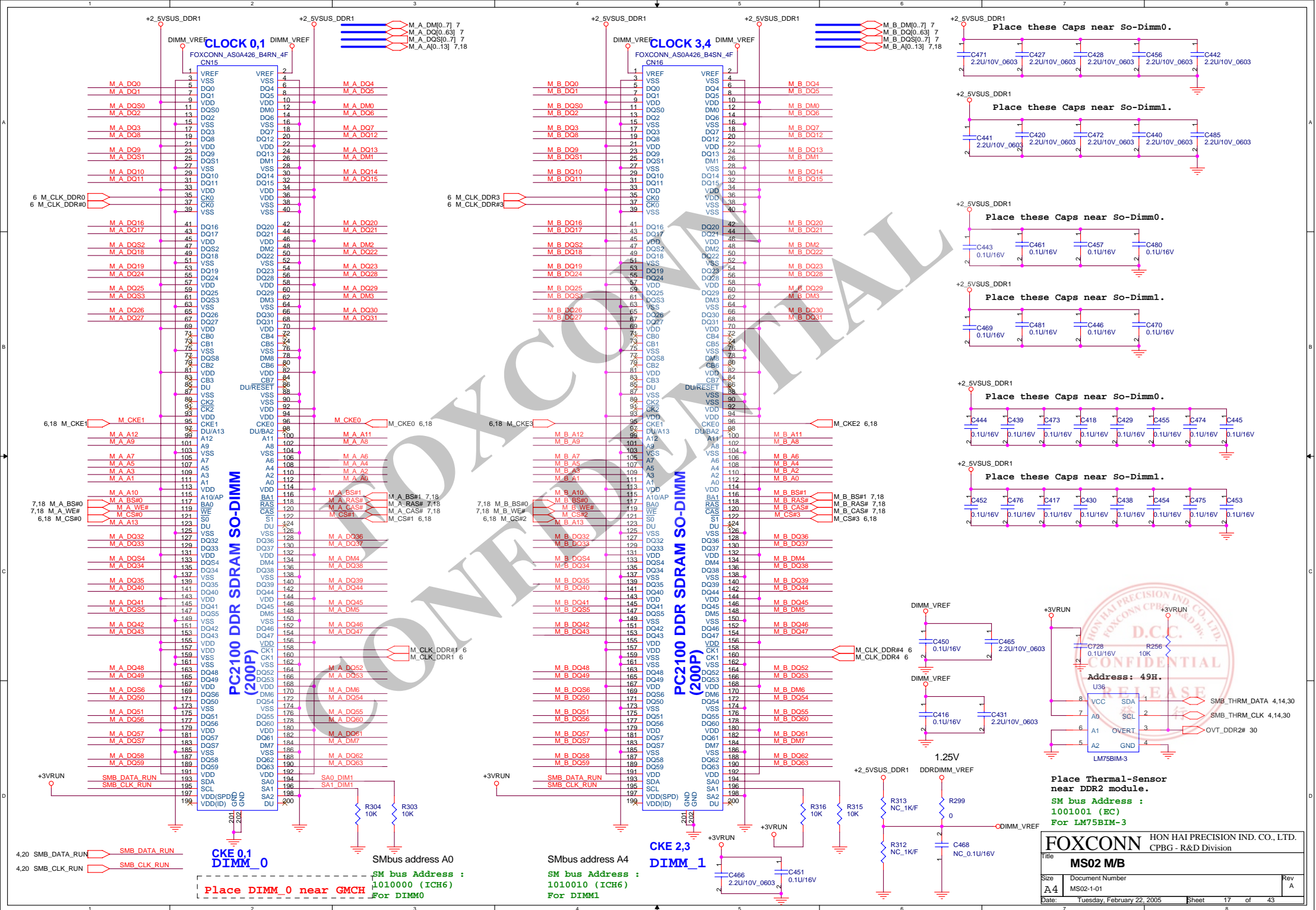






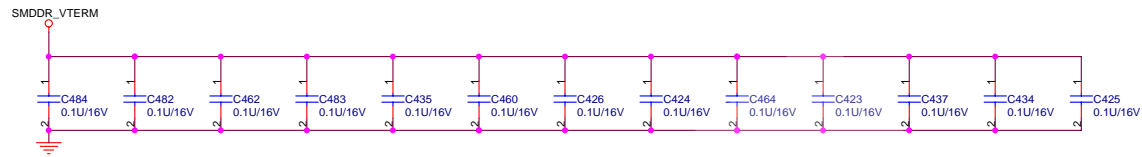




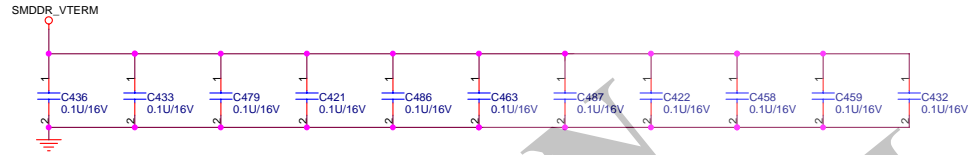




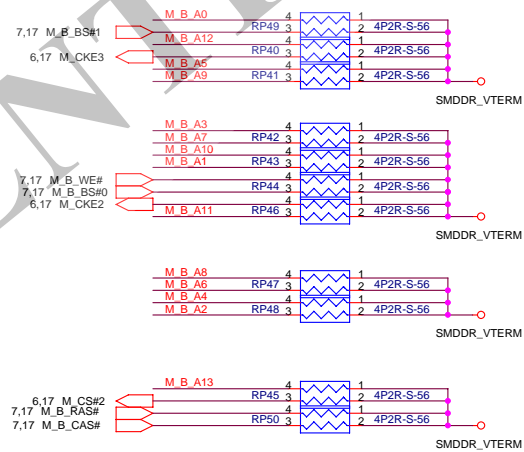
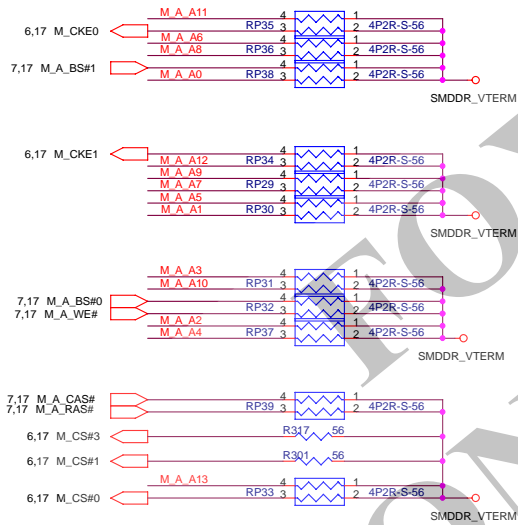
M\_A\_A[0..13] 7,17  
M\_B\_A[0..13] 7,17

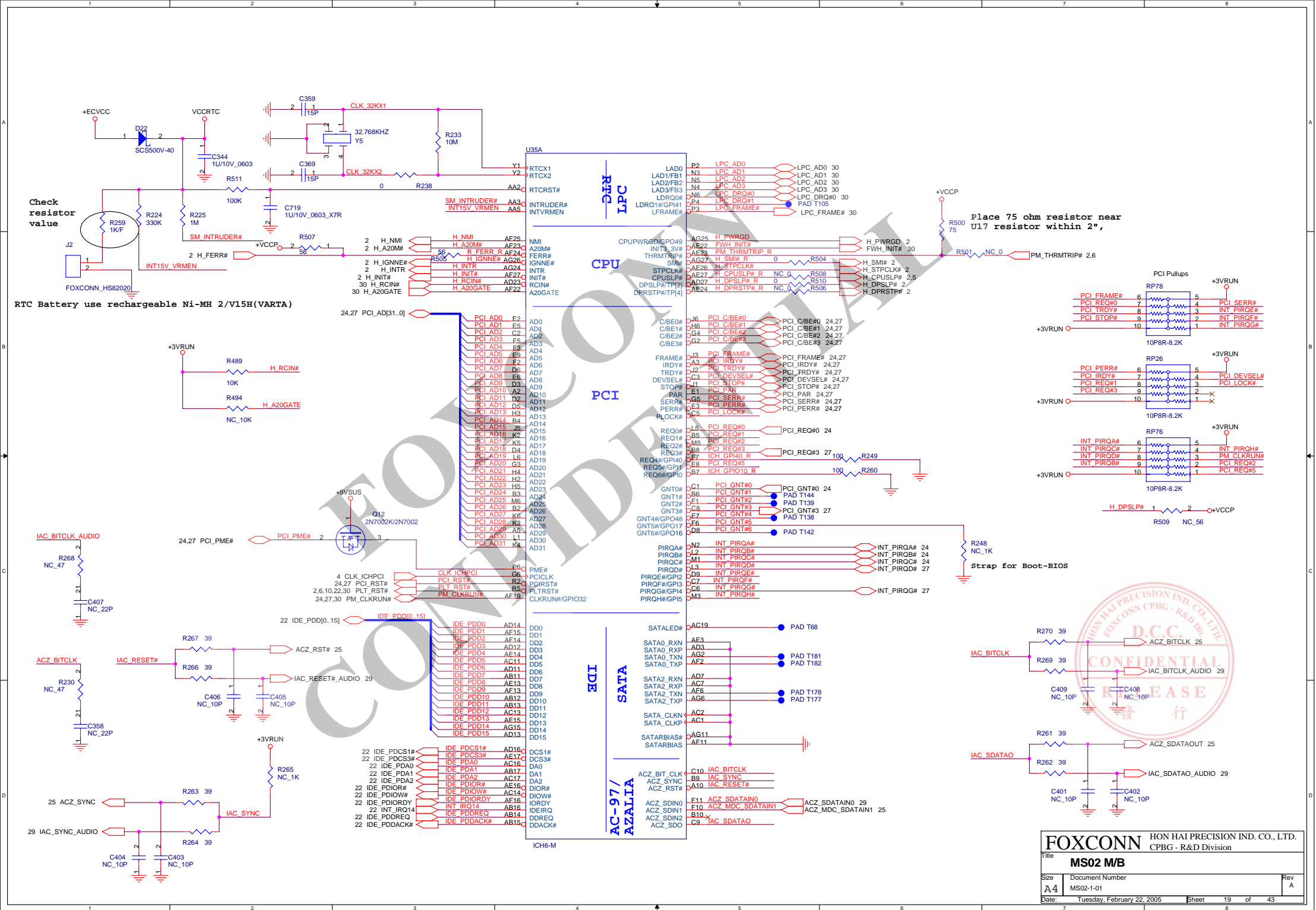


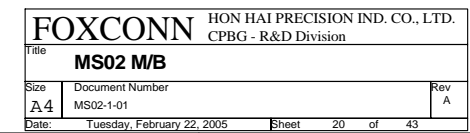
Layout note: Place 1 cap close to every 1 R-pack terminated to SMDDR\_VTERM.

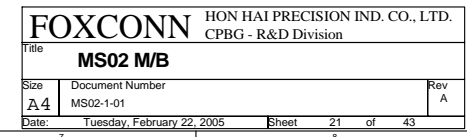


Layout note: Place 1 cap close to every 1 R-pack terminated to SMDDR\_VTERM.



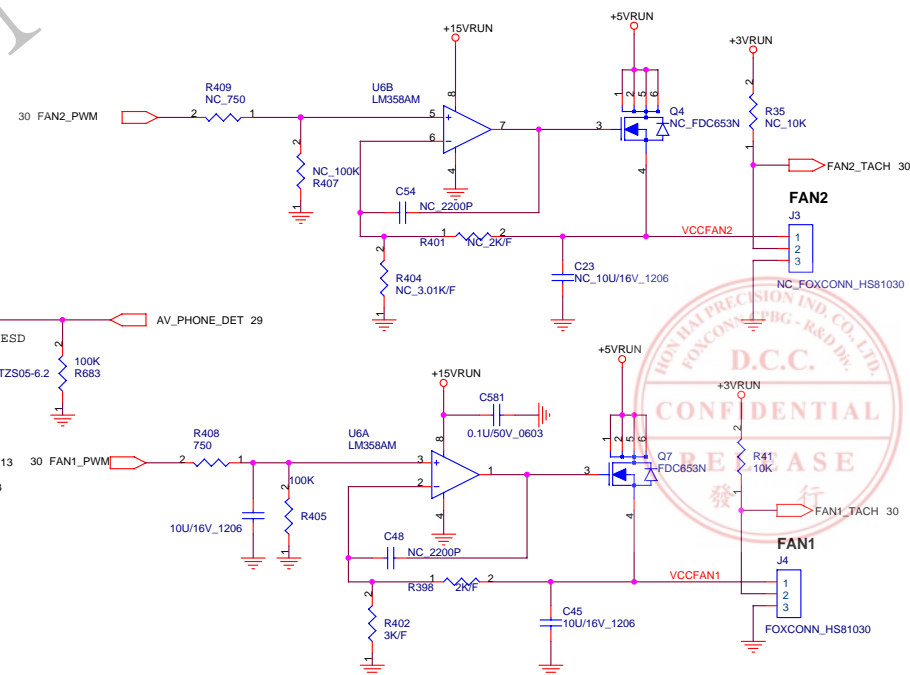
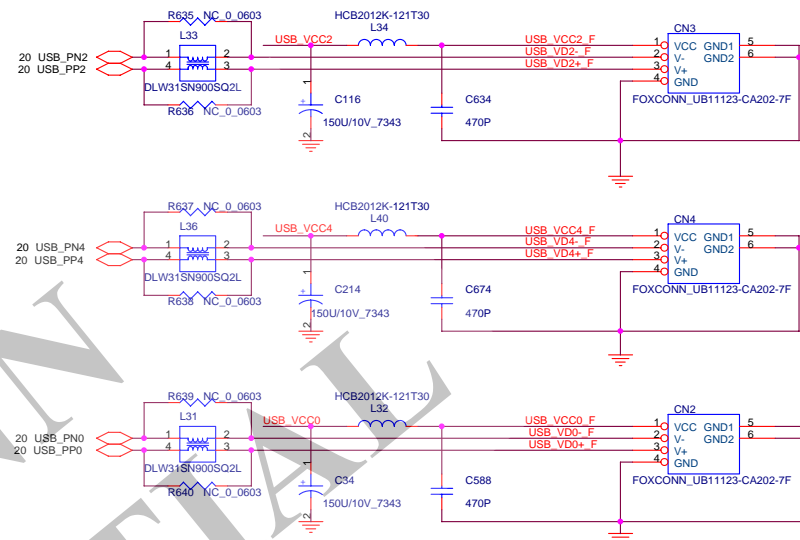








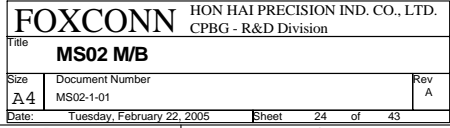


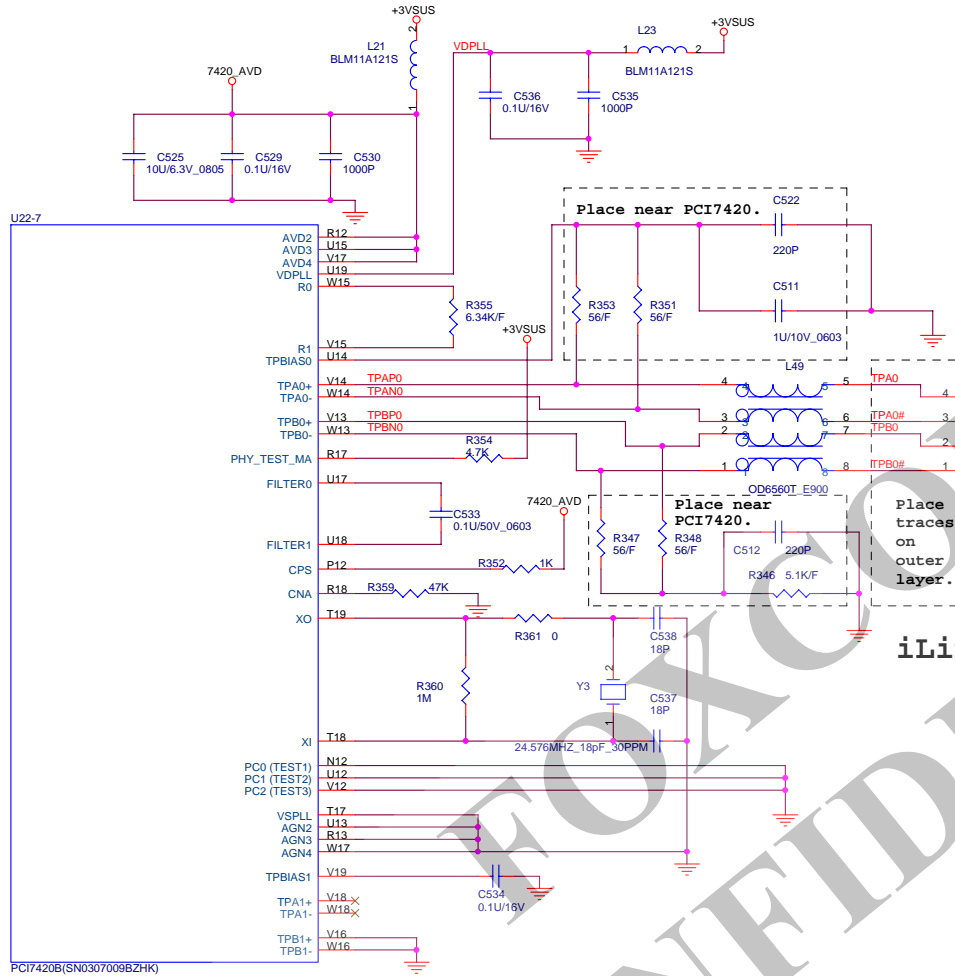


The schematic diagram illustrates the electrical connections for the FOXCONN\_QL01303\_D24A01\_5F connector. The connector pins are organized into two main sections: P1-P2 (pins 1-10) and P3-P4 (pins 11-20). The diagram shows the following connections:

- Power and Ground:**
  - P1 (Pin 1) is connected to PRT\_IN.
  - P2 (Pin 2) is connected to GND.
  - P3 (Pin 11) is connected to GND.
  - P4 (Pin 12) is connected to GND.
  - P5 (Pin 13) is connected to GND.
  - P6 (Pin 14) is connected to GND.
  - P7 (Pin 15) is connected to GND.
  - P8 (Pin 16) is connected to GND.
  - P9 (Pin 17) is connected to GND.
  - P10 (Pin 18) is connected to GND.
  - P11 (Pin 19) is connected to GND.
  - P12 (Pin 20) is connected to GND.
  - P13 (Pin 21) is connected to GND.
  - P14 (Pin 22) is connected to GND.
  - P15 (Pin 23) is connected to GND.
  - P16 (Pin 24) is connected to GND.
  - P17 (Pin 25) is connected to GND.
  - P18 (Pin 26) is connected to GND.
  - P19 (Pin 27) is connected to GND.
  - P20 (Pin 28) is connected to GND.
  - P21 (Pin 29) is connected to GND.
  - P22 (Pin 30) is connected to GND.
  - P23 (Pin 31) is connected to GND.
  - P24 (Pin 32) is connected to GND.
  - P25 (Pin 33) is connected to GND.
  - P26 (Pin 34) is connected to GND.
  - P27 (Pin 35) is connected to GND.
  - P28 (Pin 36) is connected to GND.
  - P29 (Pin 37) is connected to GND.
  - P30 (Pin 38) is connected to GND.
  - P31 (Pin 39) is connected to GND.
  - P32 (Pin 40) is connected to GND.
  - P33 (Pin 41) is connected to GND.
  - P34 (Pin 42) is connected to GND.
  - P35 (Pin 43) is connected to GND.
  - P36 (Pin 44) is connected to GND.
  - P37 (Pin 45) is connected to GND.
  - P38 (Pin 46) is connected to GND.
  - P39 (Pin 47) is connected to GND.
  - P40 (Pin 48) is connected to GND.
  - P41 (Pin 49) is connected to GND.
  - P42 (Pin 50) is connected to GND.
  - P43 (Pin 51) is connected to GND.
  - P44 (Pin 52) is connected to GND.
  - P45 (Pin 53) is connected to GND.
  - P46 (Pin 54) is connected to GND.
  - P47 (Pin 55) is connected to GND.
  - P48 (Pin 56) is connected to GND.
  - P49 (Pin 57) is connected to GND.
  - P50 (Pin 58) is connected to GND.
  - P51 (Pin 59) is connected to GND.
  - P52 (Pin 60) is connected to GND.
- Signal Connections:**
  - P1 (Pin 1) is connected to 28 DOCK\_TDP.
  - P2 (Pin 2) is connected to 28 DOCK\_TDN.
  - P3 (Pin 11) is connected to 28 DOCK\_RDP.
  - P4 (Pin 12) is connected to 28 DOCK\_RDN.
  - P5 (Pin 13) is connected to 13 PR\_DDCDATA.
  - P6 (Pin 14) is connected to 13 PR\_DDCCLK.
  - P7 (Pin 15) is connected to 13 PR\_HSYNC.
  - P8 (Pin 16) is connected to 13 PR\_VSYNC.
  - P9 (Pin 17) is connected to 29,30,33,36,38 RUN\_ON.
  - P10 (Pin 18) is connected to 29,30,33,34,36,38 SUS\_ON.
  - P11 (Pin 19) is connected to 30 D\_PWRGD.
  - P12 (Pin 20) is connected to 13,30 EN\_EXT\_DEV\_SENSE#.
  - P13 (Pin 21) is connected to 29 SPDIF\_OUT.
  - P14 (Pin 22) is connected to 29 APR\_AMP\_MUTE#.
  - P15 (Pin 23) is connected to 29 LINE\_OUT\_L+.
  - P16 (Pin 24) is connected to 29 LINE\_OUT\_L-.
  - P17 (Pin 25) is connected to 29 LINE\_OUT\_R+.
  - P18 (Pin 26) is connected to 29 LINE\_OUT\_R-.
  - P19 (Pin 27) is connected to 32 PORT\_DET\_P.
  - P20 (Pin 28) is connected to 3,30 PORT\_DET#.
  - P21 (Pin 29) is connected to 30 DOCK\_ID0.
  - P22 (Pin 30) is connected to 30 DOCK\_ID1.
  - P23 (Pin 31) is connected to 30 DOCK\_ID2.
  - P24 (Pin 32) is connected to 30 DOCK\_ID3.
  - P25 (Pin 33) is connected to 30 DOCK\_ID4.
  - P26 (Pin 34) is connected to 30 DOCK\_ID5.
  - P27 (Pin 35) is connected to 30 DOCK\_ID6.
  - P28 (Pin 36) is connected to 30 DOCK\_ID7.
  - P29 (Pin 37) is connected to 30 DOCK\_ID8.
  - P30 (Pin 38) is connected to 30 DOCK\_ID9.
  - P31 (Pin 39) is connected to 30 DOCK\_ID10.
  - P32 (Pin 40) is connected to 30 DOCK\_ID11.
  - P33 (Pin 41) is connected to 30 DOCK\_ID12.
  - P34 (Pin 42) is connected to 30 DOCK\_ID13.
  - P35 (Pin 43) is connected to 30 DOCK\_ID14.
  - P36 (Pin 44) is connected to 30 DOCK\_ID15.
  - P37 (Pin 45) is connected to 30 DOCK\_ID16.
  - P38 (Pin 46) is connected to 30 DOCK\_ID17.
  - P39 (Pin 47) is connected to 30 DOCK\_ID18.
  - P40 (Pin 48) is connected to 30 DOCK\_ID19.
  - P41 (Pin 49) is connected to 30 DOCK\_ID20.
  - P42 (Pin 50) is connected to 30 DOCK\_ID21.
  - P43 (Pin 51) is connected to 30 DOCK\_ID22.
  - P44 (Pin 52) is connected to 30 DOCK\_ID23.
  - P45 (Pin 53) is connected to 30 DOCK\_ID24.
  - P46 (Pin 54) is connected to 30 DOCK\_ID25.
  - P47 (Pin 55) is connected to 30 DOCK\_ID26.
  - P48 (Pin 56) is connected to 30 DOCK\_ID27.
  - P49 (Pin 57) is connected to 30 DOCK\_ID28.
  - P50 (Pin 58) is connected to 30 DOCK\_ID29.
  - P51 (Pin 59) is connected to 30 DOCK\_ID30.
  - P52 (Pin 60) is connected to 30 DOCK\_ID31.
  - P53 (Pin 61) is connected to 30 DOCK\_ID32.
  - P54 (Pin 62) is connected to 30 DOCK\_ID33.
  - P55 (Pin 63) is connected to 30 DOCK\_ID34.
  - P56 (Pin 64) is connected to 30 DOCK\_ID35.
  - P57 (Pin 65) is connected to 30 DOCK\_ID36.
  - P58 (Pin 66) is connected to 30 DOCK\_ID37.
  - P59 (Pin 67) is connected to 30 DOCK\_ID38.
  - P60 (Pin 68) is connected to 30 DOCK\_ID39.
  - P61 (Pin 69) is connected to 30 DOCK\_ID40.
  - P62 (Pin 70) is connected to 30 DOCK\_ID41.
  - P63 (Pin 71) is connected to 30 DOCK\_ID42.
  - P64 (Pin 72) is connected to 30 DOCK\_ID43.
  - P65 (Pin 73) is connected to 30 DOCK\_ID44.
  - P66 (Pin 74) is connected to 30 DOCK\_ID45.
  - P67 (Pin 75) is connected to 30 DOCK\_ID46.
  - P68 (Pin 76) is connected to 30 DOCK\_ID47.
  - P69 (Pin 77) is connected to 30 DOCK\_ID48.
  - P70 (Pin 78) is connected to 30 DOCK\_ID49.
  - P71 (Pin 79) is connected to 30 DOCK\_ID50.
  - P72 (Pin 80) is connected to 30 DOCK\_ID51.
  - P73 (Pin 81) is connected to 30 DOCK\_ID52.
  - P74 (Pin 82) is connected to 30 DOCK\_ID53.
  - P75 (Pin 83) is connected to 30 DOCK\_ID54.
  - P76 (Pin 84) is connected to 30 DOCK\_ID55.
  - P77 (Pin 85) is connected to 30 DOCK\_ID56.
  - P78 (Pin 86) is connected to 30 DOCK\_ID57.
  - P79 (Pin 87) is connected to 30 DOCK\_ID58.
  - P80 (Pin 88) is connected to 30 DOCK\_ID59.
  - P81 (Pin 89) is connected to 30 DOCK\_ID60.
  - P82 (Pin 90) is connected to 30 DOCK\_ID61.
  - P83 (Pin 91) is connected to 30 DOCK\_ID62.
  - P84 (Pin 92) is connected to 30 DOCK\_ID63.
  - P85 (Pin 93) is connected to 30 DOCK\_ID64.
  - P86 (Pin 94) is connected to 30 DOCK\_ID65.
  - P87 (Pin 95) is connected to 30 DOCK\_ID66.
  - P88 (Pin 96) is connected to 30 DOCK\_ID67.
  - P89 (Pin 97) is connected to 30 DOCK\_ID68.
  - P90 (Pin 98) is connected to 30 DOCK\_ID69.
  - P91 (Pin 99) is connected to 30 DOCK\_ID70.
  - P92 (Pin 100) is connected to 30 DOCK\_ID71.
  - P93 (Pin 101) is connected to 30 DOCK\_ID72.
  - P94 (Pin 102) is connected to 30 DOCK\_ID73.
  - P95 (Pin 103) is connected to 30 DOCK\_ID74.
  - P96 (Pin 104) is connected to 30 DOCK\_ID75.
  - P97 (Pin 105) is connected to 30 DOCK\_ID76.
  - P98 (Pin 106) is connected to 30 DOCK\_ID77.
  - P99 (Pin 107) is connected to 30 DOCK\_ID78.
  - P100 (Pin 108) is connected to 30 DOCK\_ID79.
  - P101 (Pin 109) is connected to 30 DOCK\_ID80.
  - P102 (Pin 110) is connected to 30 DOCK\_ID81.
  - P103 (Pin 111) is connected to 30 DOCK\_ID82.
  - P104 (Pin 112) is connected to 30 DOCK\_ID83.
  - P105 (Pin 113) is connected to 30 DOCK\_ID84.
  - P106 (Pin 114) is connected to 30 DOCK\_ID85.
  - P107 (Pin 115) is connected to 30 DOCK\_ID86.
  - P108 (Pin 116) is connected to 30 DOCK\_ID87.
  - P109 (Pin 117) is connected to 30 DOCK\_ID88.
  - P110 (Pin 118) is connected to 30 DOCK\_ID89.
  - P111 (Pin 119) is connected to 30 DOCK\_ID90.
  - P112 (Pin 120) is connected to 30 DOCK\_ID91.
  - P113 (Pin 121) is connected to 30 DOCK\_ID92.
  - P114 (Pin 122) is connected to 30 DOCK\_ID93.
  - P115 (Pin 123) is connected to 30 DOCK\_ID94.
  - P116 (Pin 124) is connected to 30 DOCK\_ID95.
  - P117 (Pin 125) is connected to 30 DOCK\_ID96.
  - P118 (Pin 126) is connected

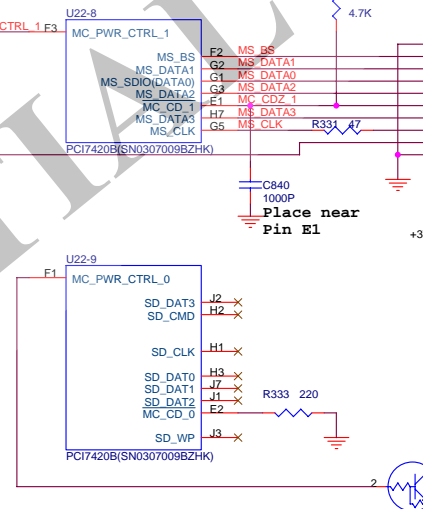
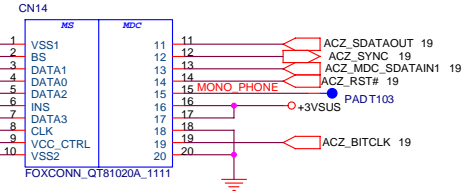
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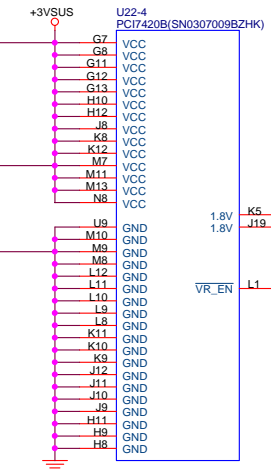


iLink CONN.

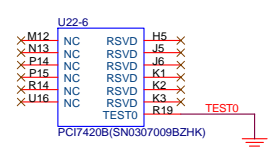
## MS / MDC CONN.



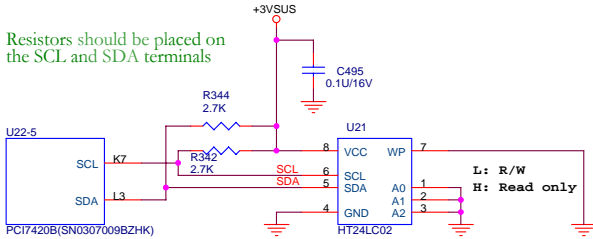
## PCI7420 Power Terminals



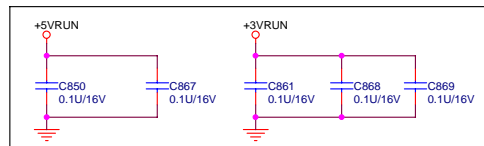
## PCI7420 UNUSED TERMINALS



Resistors should be placed on the SCL and SDA terminals



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For EMI .



Near RP51 ,  
for EMI .



Near R607 ,  
for EMI .



Near Q7 ,  
for EMI .



Near Q4 ,  
for EMI .



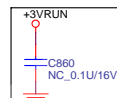
Near U37 ,  
for EMI .



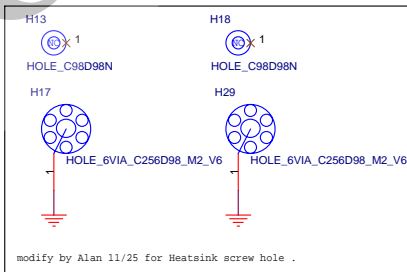
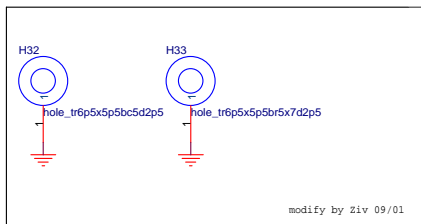
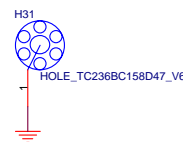
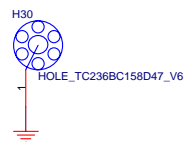
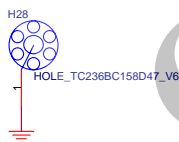
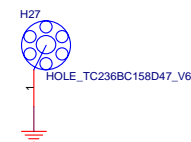
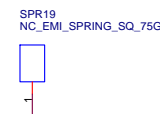
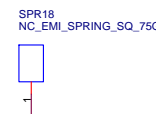
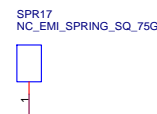
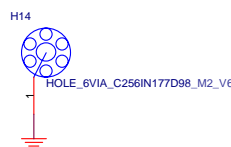
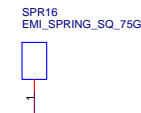
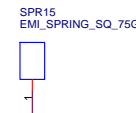
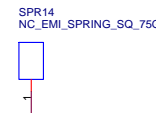
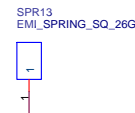
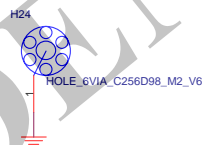
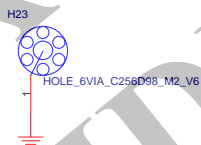
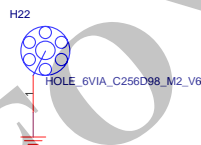
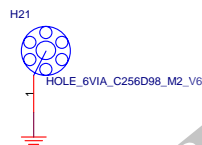
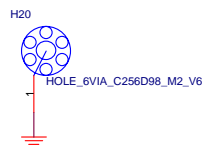
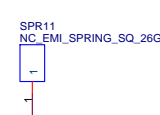
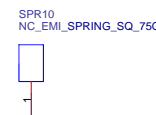
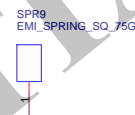
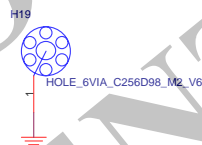
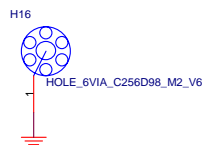
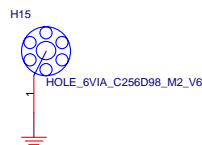
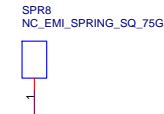
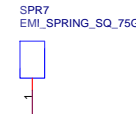
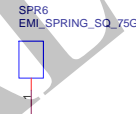
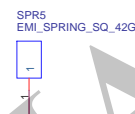
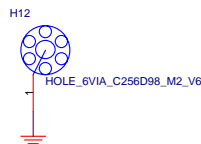
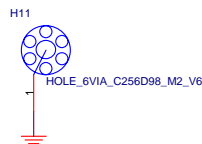
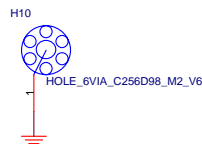
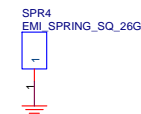
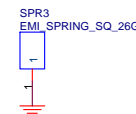
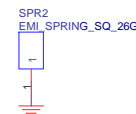
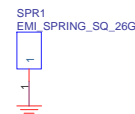
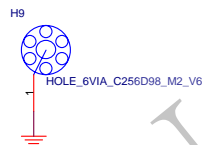
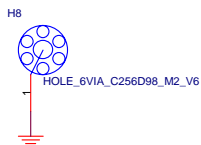
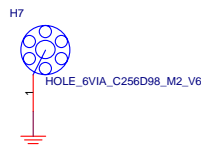
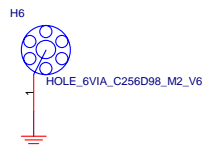
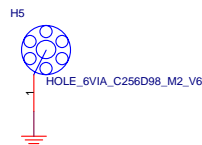
Near Q3 ,  
for EMI .



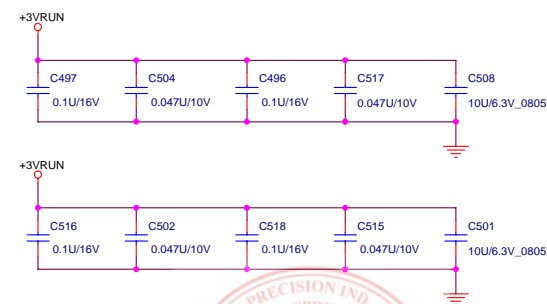
Near Q4 ,  
for EMI .



Near D8 ,  
for EMI .

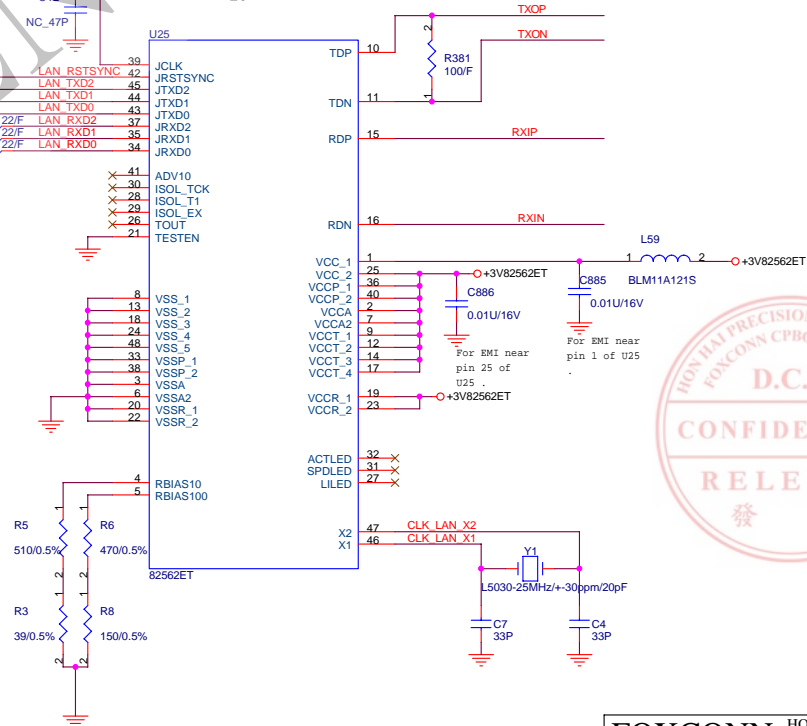
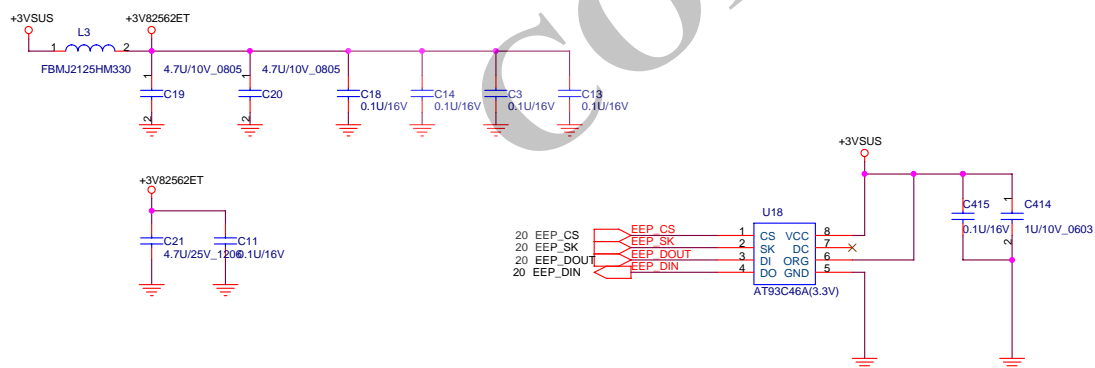
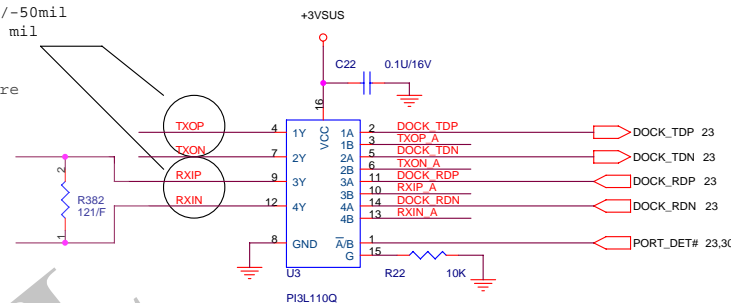


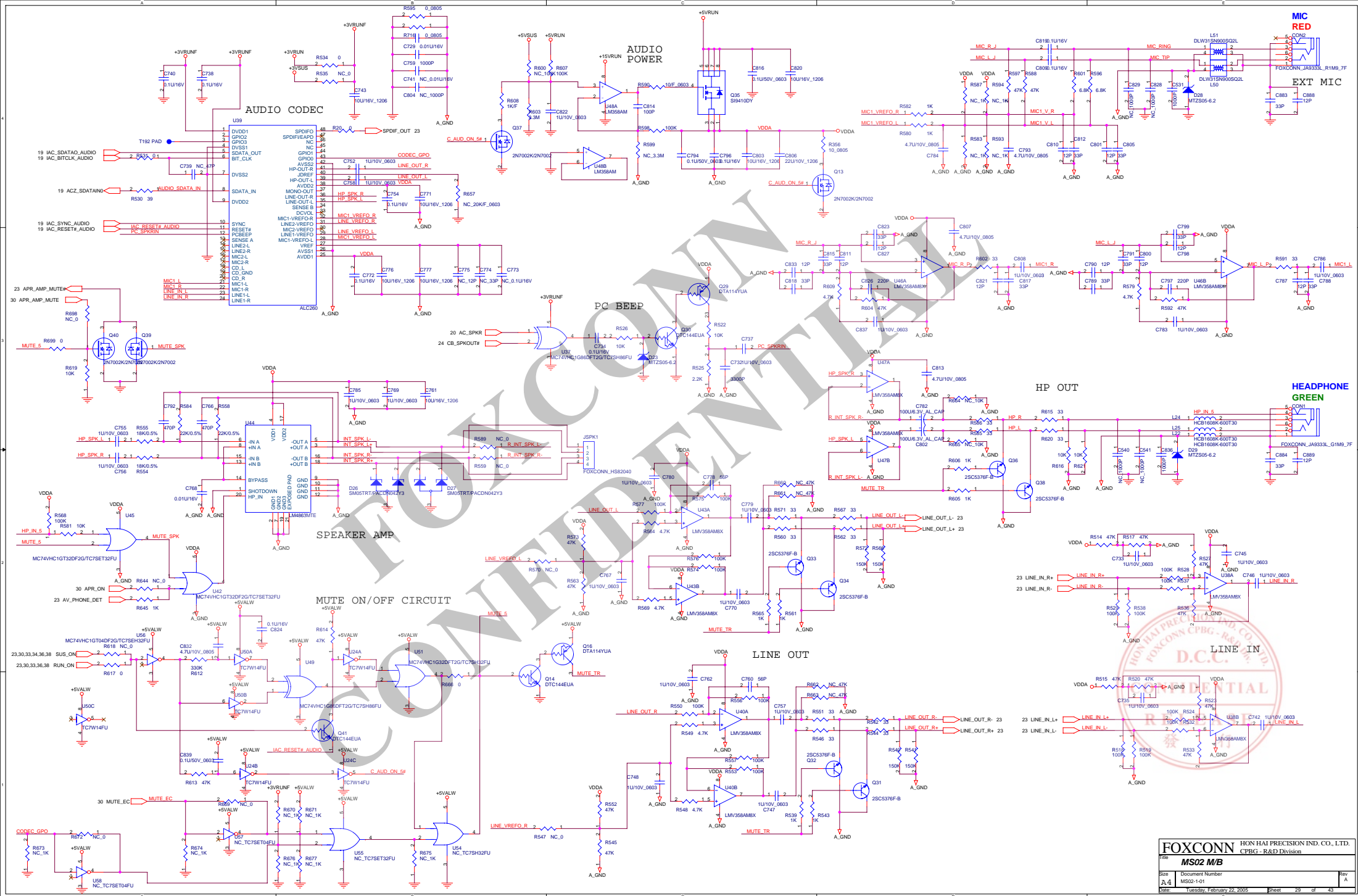
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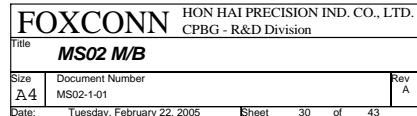


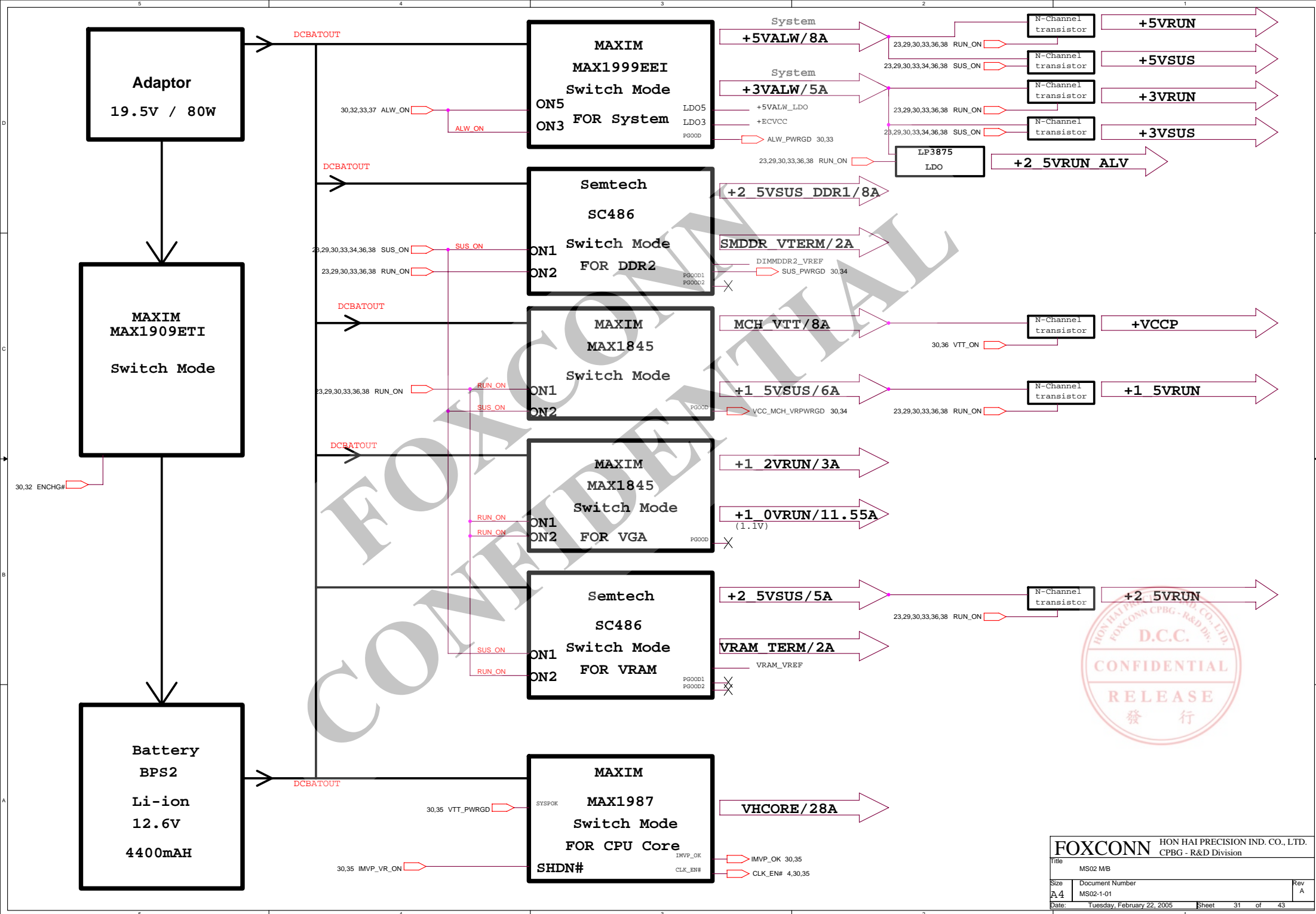


Match total length of chip side Rx and Tx pair traces +/-50mil  
Match length of cable side Rx and Tx pair traces +/- 50 mil  
Total line TX+ to TX- and RX- and RX+ should be matched within 50 mils.  
Keep 50mil space between pairs and other traces. Pairs are 100ohm differential,





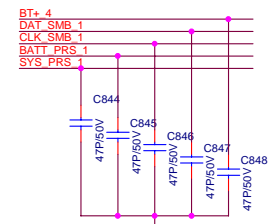
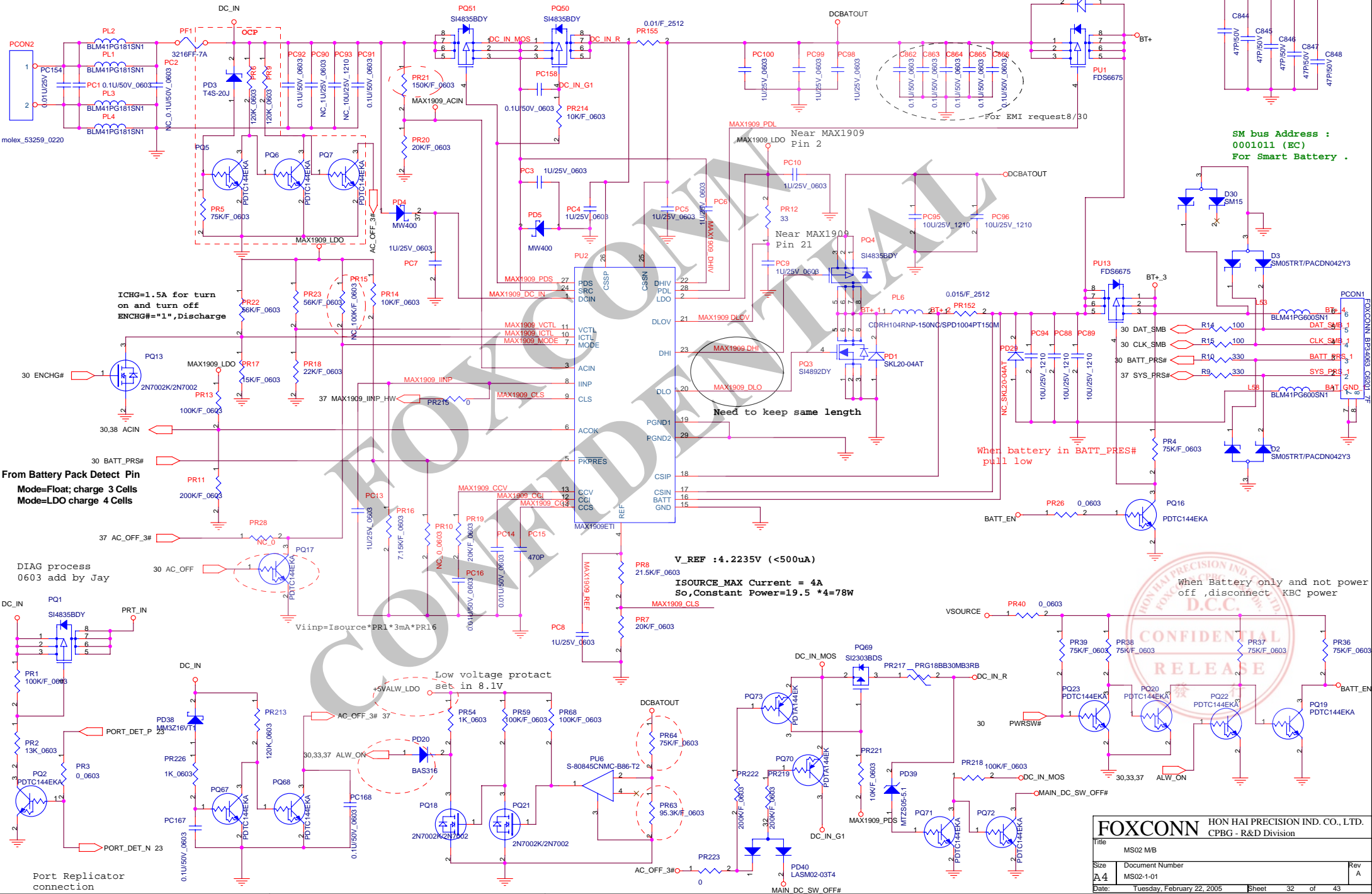




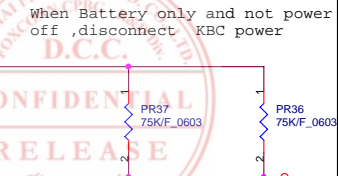
DC\_IN from port replicator

AC\_IN Threshold 2.089V Max.  
AC\_IN > 2.089V --> AC DETECT

DC\_IN



SM bus Address :  
0001011 (EC)  
For Smart Battery .



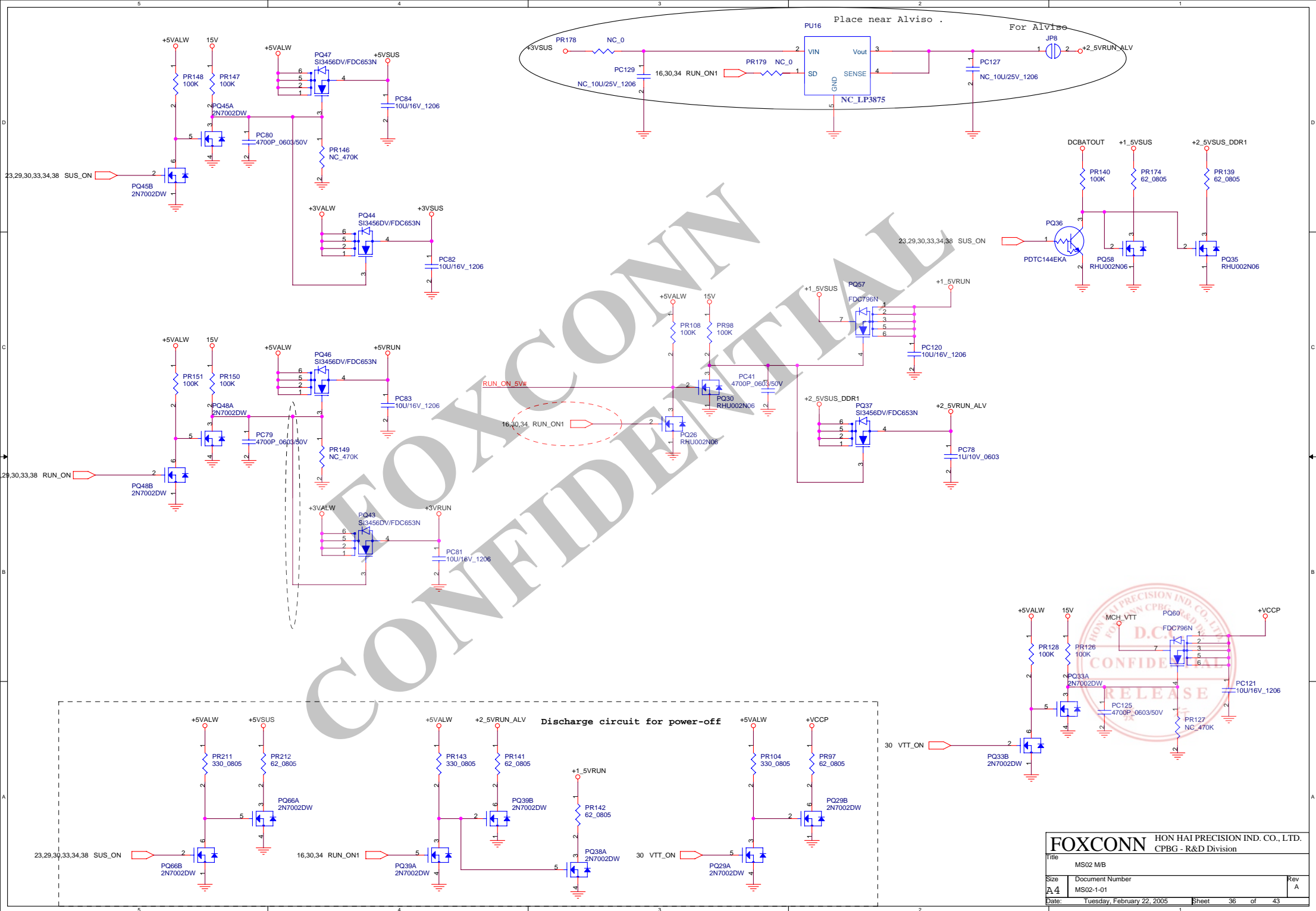
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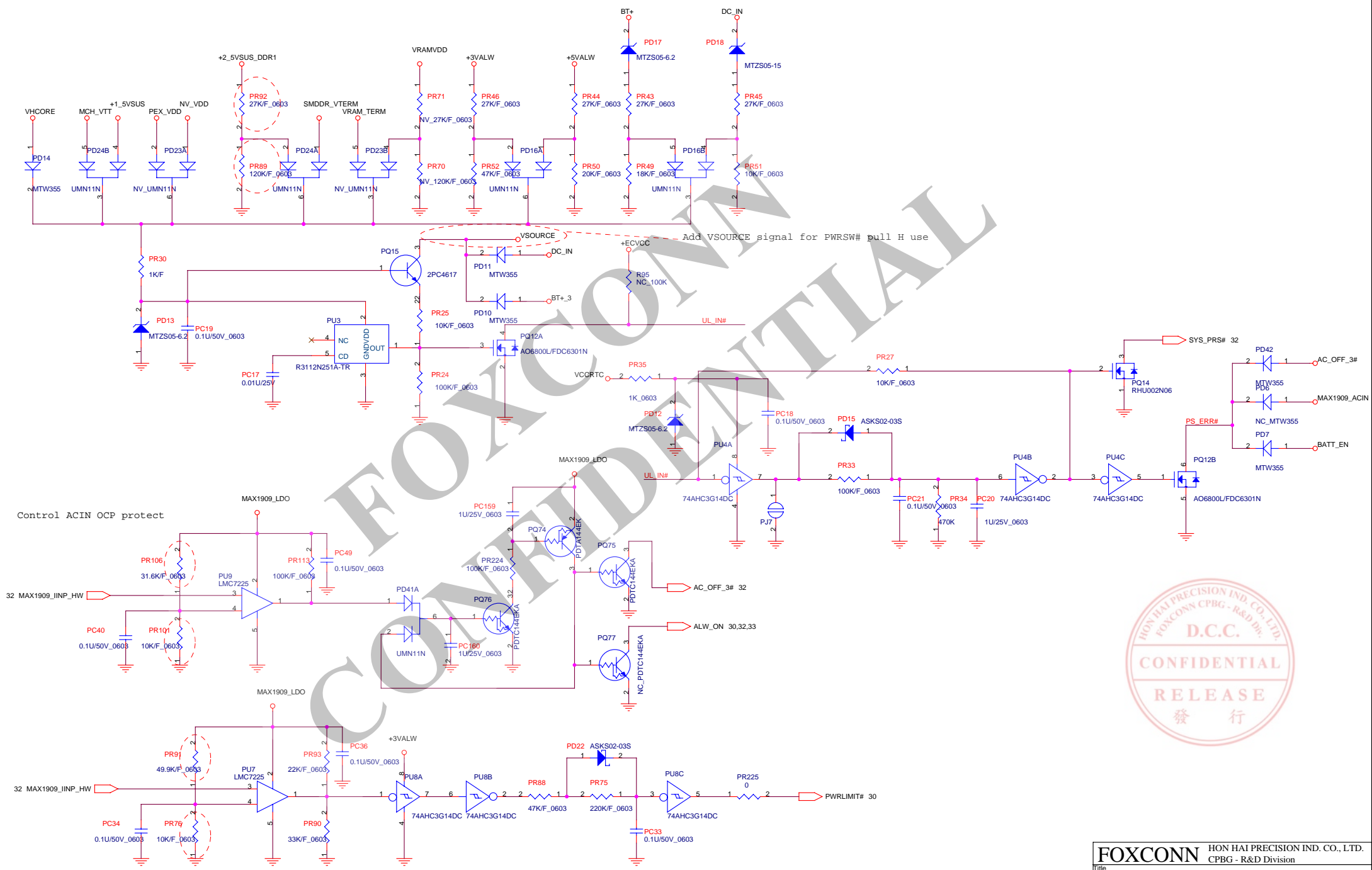






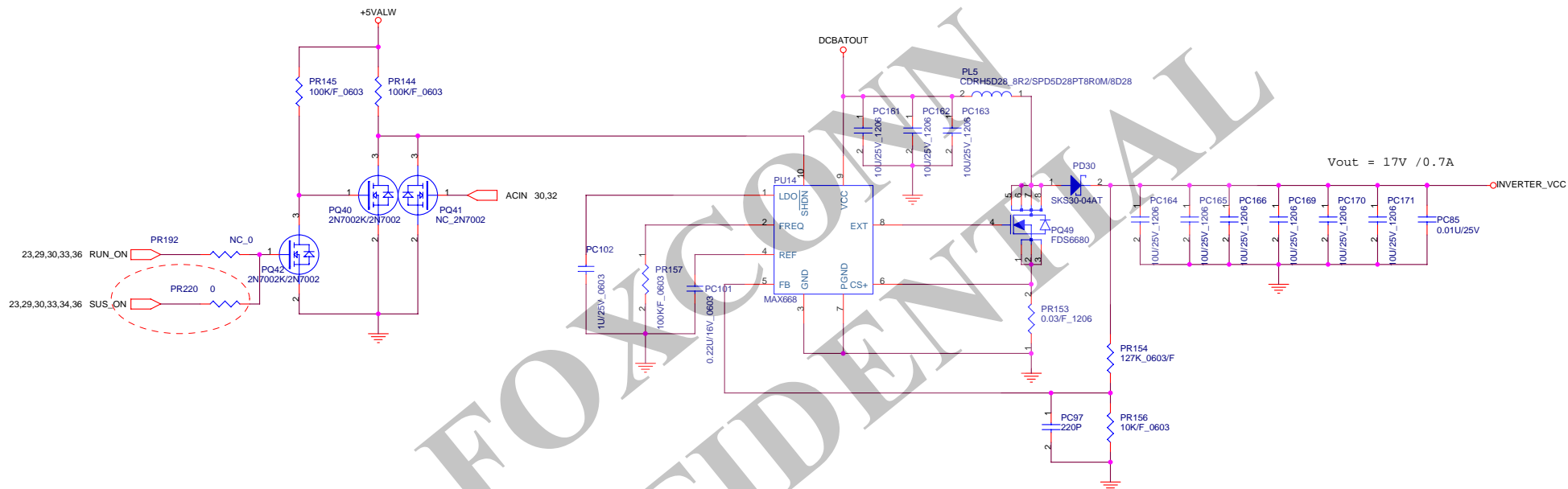






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HISTORY  
(2005/01/06)

- P.29 Change value;L50,L51 CM-CHOKE to DLW31SN900SQ2L
- P.29 Change R550.R553.R556.R557.R574.R575.R576.R577,R518.R519.R529.R538 from 10K to 100K of audio for RS01
- P.29 Change R540.R541.R566.R572 from 47K to 150K of audio for RS01
- P.29 Change C760,C778 from 470P to 56P of audio for RS01
- P.29 Delete C730.C731.C736.C744 of audio for RS01

(2005/01/09)

- P.32 Change footprint;PD1 DIODE\_2P\_138\_185X95 to diode\_2p\_177\_185x95

(2005/01/10)

- P.30 Change R282 100K to NC\_100K,R295 NC\_100K to 100K,of system ID

(2005/01/17)

- P.30 Change value;U20 EN29LV800AB-70TC to MBM29LV800BE-70TN-KE1
- P.15 Change value;U28,U31 NV\_HY5DU283222AF to HY5DU283222BFP-33
- P.32 Changevalue;D2,D3,D26,D27,D31,D32PACDN042Y3 to SM05TRT
- P.13 Change value; U1 74AHC1G08GW to SN74AHC1G08DCKR
- P.13 Change value; U26 74LVC1G08GW to SN74LVC1G08DCKR
- P.29 Change value; U56 TC7SET04FU to MC74VHC1GT04DF2G
- P.29 Change value; U42,U45 TC7SET32FU to MC74VHC1GT32DF2G
- P.29 Change value; U51 TC7SH32FU to MC74VHC1G32DFT2G
- P.29 Change value; U37,U49 TC7SH86FU to MC74VHC1G86DFT2G
- P.30 Change value; U23 NC7S32 to SN74AHC1G32DBVR
- P.22 Change value; C399 100U/10V\_7343 to 100U/6.3V\_7343
- P.23 Change value; C34,C116,C214 150U/10V\_7343 to 150U/6.3V\_7343
- P.03 Change value; C707 150U/10V\_7343 to 150U/6.3V\_7343
- P.08 Change value; C871 AL\_150U/10V\_7343 to 150U/6.3V\_7343
- P.33 Change value; PC152,PC153,PC133,PC134 6TPE150M\_7343 to ECGUD0J151R\_7343
- P.35 Change value; PC114,PC115,PC116,PC117,PC156,PC157 2R5TPE220M9 to EEFSX0D221ER
- P.34 Change value; PC38,PC44,PC122,PC123,PC124 2R5TPE330M9\_7343 to EEFSX0D331ER\_7343
- P.08 Change value; C677,C697,C714,C715 470U/2.5V\_7343 to 470U/2V\_7343
- P.10 Change value; C874 NV\_470U/2.5V\_7343 to NV\_470U/2V\_7343
- P.32 Change value; PQ69,PQ28 IRLML5103 to SI2303BDS
- P.02 Change value; Q27 2N7002 to 2N7002E
- P.13 Change value; Q2,Q3,Q18,Q20 2N7002 to 2N7002E
- P.19 Change value; Q12 2N7002 to 2N7002E
- P.27 Change value; Q15 2N7002 to 2N7002E
- P.29 Change value; Q13,Q37,Q39,Q40 2N7002 to 2N7002E
- P.30 Change value; Q5,Q9 2N7002 to 2N7002E
- P.32 Change value; PQ13,PQ18,PQ21 2N7002 to 2N7002E
- P.38 Change value; PQ40,PQ42 2N7002 to 2N7002E
- P.37 Change value; PQ12 FDC6301N to AO6800L
- P.36 Change value; PQ37,PQ43,PQ44,PQ46,PQ47 FDC653N to SI3456DV
- P.23 Change value; Q7 FDC653N to SI3456DV
- P.13 Change value; Q21 FDC653N to SI3456DV
- P.34 Change value; PL9 SPD1004PT3R8M to CDRH104RNP-3R8NC
- P.32 Change value; PL6 SPD1004PT150M to CDRH104RNP-150NC
- P.33 Change value; PL13 SPD1004PT5R2M to CDRH104RNP-5R2NC
- P.34 Change value; PL10,PL11 SPD1005PT1R5M to CDRH105RNP-1R5NC
- P.38 Change value; PL5 SPD5D28PT8R0M to CDRH5D28\_8R2
- P.25 Change value; CN6 FOXCONN\_UV31413\_VU81P\_7F to FOXCONN\_UV31413-AU81P-7F
- P.23 Change value; CN2,CN3,CN4 FOXCONN\_UB11123\_CA201\_7F to FOXCONN\_UB11123-CA202-7F
- P.13 Change value; JVGAl FOXCONN\_DZ11A91\_MW222\_4F to FOXCONN\_DZ11A91-MA222-4F
- P.29 Change value; R575 10K to 100K of audio for RS01



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HISTORY

(2005/01/20)

P.10 Change R417,R426 NC\_2K to NV\_2K,R416,R425 NV\_2K to NC\_2K,of VRAM ID

P.30 Add value source;U20 EN29LV800AB-70TC

P.15 Add value source;U28,U31 NV\_HY5DU283222AF

P.32 Add value source;D2,D3,D26,D27,D31,D32 PACDN042Y3

P.13 Add value source; U1 74AHC1G08GW

P.13 Add value source; U26 74LVC1G08GW

P.29 Add value source; U56 TC7SET04FU

P.29 Add value source; U42,U45 TC7SET32FU

P.29 Add value source; U51 TC7SH32FU

P.29 Add value source; U37,U49 TC7SH86FU

P.30 Add value source; U23 NC7S32

P.22 Add value source; C399 100U/10V\_7343

P.23 Add value source; C34,C116,C214 150U/10V\_7343

P.03 Add value source; C707 150U/10V\_7343

P.08 Add value source; C871 AL\_150U/10V\_7343 to 150U/6.3V\_7343

P.33 Add value source; PC152,PC153,PC133,PC134 6TPE150M\_7343

P.35 Add value source; PC114,PC115,PC116,PC117,PC156,PC157 2R5TPE220M9

P.34 Add value source; PC38,PC44,PC122,PC123,PC124 2R5TPE330M9\_7343

P.08 Add value source; C677,C697,C714,C715 470U/2.5V\_7343

P.10 Add value source; C874 NV\_470U/2.5V\_7343

P.32 Add value source; PQ69,PQ28 IRLML5103

P.02 Add value source; Q27 2N7002

P.13 Add value source; Q2,Q3,Q18,Q20 2N7002

P.19 Add value source; Q12 2N7002

P.27 Add value source; Q15 2N7002

P.29 Add value source; Q13,Q37,Q39,Q40 2N7002

P.30 Add value source; Q5,Q9 2N7002

P.32 Add value source; PQ13,PQ18,PQ21 2N7002

P.38 Add value source; PQ40,PQ42 2N7002

P.37 Add value source; PQ12 FDC6301N

P.36 Add value source; PQ37,PQ43,PQ44,PQ46,PQ47 FDC653N

P.23 Add value source; Q7 FDC653N

P.13 Add value source; Q21 FDC653N

P.34 Add value source; PL9 SPD1004PT3R8M

P.32 Add value source; PL6 SPD1004PT150M

P.33 Add value source; PL13 SPD1004PT5R2M

P.34 Add value source; PL10,PL11 SPD1005PT1R5M

P.38 Add value source; PL5 SPD5D28PT8R0M

(2005/01/21)

P.29 Add CAP 33P,C895,C898,C900,C901,C902,C904,C905,C906 for Audio GPRS noise

P.29 Add CAP 12P,C896,C897,C899,C903 for Audio GPRS noise

(2005/01/21)

P.30 Change value;C447,C448 15P to 10P of crystal precision

(2005/01/26)

P.10 Change value;R425,R416 NC\_2K to H\_NV\_2K for Hynix VRAM

P.10 Change value;R426,R417 NV\_2K to S\_NV\_2K for Samsung VRAM

P.01 Add BOM configuration;Hynix:H\_ NV\_ , Samsung: S\_NV\_ .of VRAM ID

(2005/01/28)

P.29 Del CAP 33P,C895,C898,C900,C901,C902,C904,C905,C906,C896,C897,C899,C903

(2005/02/01)

P.29 Add CAP 33P,C895,C898,C900,C901,C902,C904,C905,C906 for Audio GPRS noise

P.29 Add CAP 12P,C896,C897,C899,C903 for Audio GPRS noise

P.29 Add RES,R716 0\_0805

Change ";" to "/" for correct ORCAD rule :

C399,C677,C697,C707,C714,C715,C871,C874,D2,D3,D26,D27,D31,D32,PC38,PC44,PC114,PC115,PC116,PC117,PC122,PC123,PC124,PC133,PC134,PC152,PC153,PC156,PC157,PL5,PL6,PL9,PL10,PL11,PL13,PQ12,PQ13,PQ18,PQ21,PQ37,PQ40,PQ42,PQ43,PQ44,PQ46,PQ47,Q2,Q3,Q5,Q9,Q12,Q13,Q18,Q20,Q21,Q27,Q37,Q39,Q40,U1,U20,U23,U26,U28,U31,U37,U42,U45,U49,U51,U56.

Correct page order & change Rev.01. to Rev 0.2 .



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HISTORY

(2005/02/14)  
P.29 Del CAP C895,C898,C900,C901,C902,C904,C905,C906,C896,C897,C899,C903;Del RES R716  
Correct page order & change Rev.02. to Rev 0.3.

(2005/02/16)  
P.28 Add L59 BLM11A121S from EMI suggest  
P.28 Del U25 pin 1 & U25 pin 25 connector line from EMI suggest

(2005/02/17)  
P.29 Add RES R716 NC\_0\_0805,for EMI suggest

(2005/02/19)  
P.37 Add diode PD42 connect AC\_OFF\_3#.for power issue UL latch  
P.37 Change PD6 value MTW355 to NC\_MTW355 for UL latch  
P.04 change R454 4.7K to 1K for OVT\_EC# singal pull-low  
P.04 Add RES 10K,R717. for OVT\_EC# singal pull-low  
P.38 Add PL5 Value CDRH5D28\_8R2/SPD5D28PT8R0M to CDRH5D28\_8R2/SPD5D28PT8R0M/8D28 for PQ49 failed  
P.38 change PR153 value 0.02/F\_1206 to 0.03/F\_1206 for PQ49 failed  
P.38 Add 3 CAP,PC169,PC170,PC171 10U/25V\_1206 for PQ49 failed  
P.38 change PL5 footprint CHOKE\_2P\_163\_217x217 to CHOKE\_327x327\_217x217use for PQ49 failed

(2005/02/20)  
P.38 change PL5 footprint CHOKE\_327x327\_217x217use to choke\_2p\_319\_327x327\_h118 for PQ49 failed

(2005/02/21)  
P.29 Del C836,C531;Add RES R718,R719 0 ohm for EMI solution.

(2005/02/22)  
P.29 Del R718,R719. Add C836,C531 CAP 1000P for EMI solution  
P.29 Change R716 value NC\_0\_0805 to 0\_0805 for EMI solution



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## Revision History

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